

**TITLE : NV140FHM-N4H****Product Specification****Rev. 0****( DELL DPN :2X30K)****BOE Optoelectronics Technology Co., Ltd**

SPEC. NUMBER

PRODUCT GROUP

Rev.

ISSUE DATE

PAGE

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TFT-LCD

0

2018.09.04

1 OF 33

## REVISION HISTORY

( ) Preliminary Specification

(√) Final Specification

Revision No.	Page	Description of Changes	Date	Prepared
0	33	Initial Version	2018/9/4	Yang, zhaoman

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# 1.0 GENERAL DESCRIPTION

## 1.1 Introduction

NV140FHM-N4H is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 14.0 inch diagonally measured active area with Full-HD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 262k(6bit) colors and color gamut 45%. The TFT-LCD panel used for this module is a low reflection and higher color type. Therefore, this module is suitable for Notebook PC. The LED driver for back-light driving is built in this model.

All input signals are eDP1.2 interface compatible.

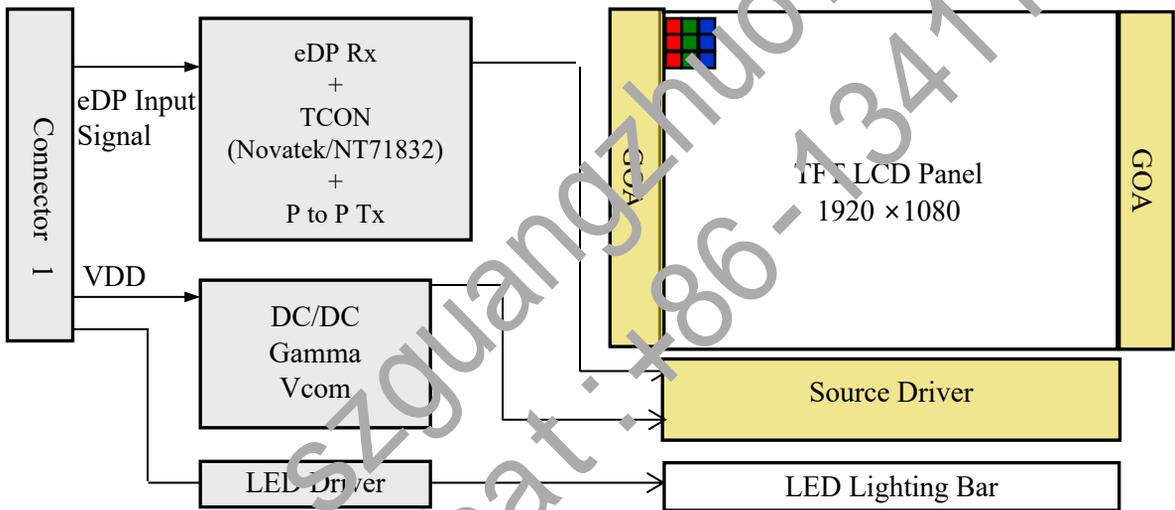


Figure 1. Drive Architecture

## 1.2 Features

- 2 lane eDP interface with 2.7Gbps link rates
- Thin and light weight
- 262k(6bit) color depth, color gamut 45%
- Single LED lighting bar (Bottom side/Horizontal direction)
- Data enable signal mode
- Side mounting frame
- Green product (RoHS & Halogen free product)
- On board LED driving circuit
- Low driving voltage and low power consumption
- On board EDID chip

### 1.3 Application

- Notebook PC (Wide type)

### 1.4 General Specification

The followings are general specifications at the model NV140FHM-N4H. (listed in Table 1.)

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	309.312 (H) x 173.998(V)	mm	
Number of pixels	1920 (H) x 1080 (V)	pixels	
Pixel pitch	161.1 (H) x 161.1 (V)	um	
Pixel arrangement	RGB vertical stripe		
Display colors	262k(6bit)		
Color gamut	45%		
Display mode	Normally black		
Dimensional outline	(315.812±0.3)*(197.589±0.5)*(2.85±0.15)(W PCB) (315.812±0.3)*(186.069±0.3)*(2.85±0.15)(W/O PCB)	mm	
Weight	285(max)	g	
Surface treatment	Anti-Glare		
Surface hardness	3H		
Back-light	Bottom edge side, 1-LED lighting bar type		Note 1
Power consumption	P <sub>D</sub> : 0.85	W	@Mosaic
	P <sub>BL</sub> : 2.2max	W	
	P <sub>Total</sub> : 3.05	W	@Mosaic

Notes : 1. LED Lighting Bar (32\*LED Array)

## 2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings >

Ta=25+/-2°C

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V <sub>DD</sub>	-0.3	4.0	V	Note 1
Logic Supply Voltage	V <sub>IN</sub>	V <sub>ss</sub> -0.3	V <sub>DI</sub> +0.3	V	
Operating Temperature	T <sub>OP</sub>	0	+70	°C	Note 2
Storage Temperature	T <sub>ST</sub>	-20	+60	°C	

Notes :

1. Permanent damage to the device may occur if maximum values are exceeded functional operation should be restricted to the condition described under normal operating conditions.
2. Temperature and relative humidity range are shown in the figure below.  
95 % RH Max. ( 40 °C ≥ Ta ) Maximum wet - bulb temperature at 39 °C or less. ( Ta > 40 °C ) No condensation.

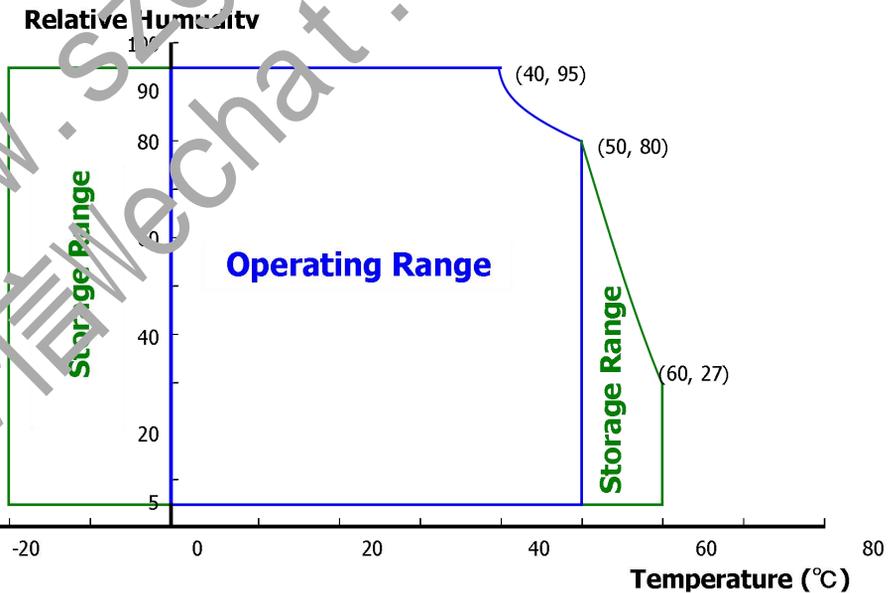


Figure 2. Temperature and Relative Humidity Range

### 3.0 ELECTRICAL SPECIFICATIONS

#### 3.1 Electrical Specifications

< Table 3. Electrical Specifications >

Ta=25+/-2°C

Parameter		Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V <sub>DD</sub>	3.0	3.3	3.6	V	Note 1
Permissible Input Ripple Voltage	V <sub>RF</sub>	-10%* V <sub>DD</sub>	-	10%*V <sub>DD</sub>	mV	Note 4
BIST Control Level	High Level	2	-	3.6	V	
	Low Level	0	-	0.8	V	
CABC Control Level	High Level	2	-	3.6	V	
	Low Level	0	-	0.8	V	
Power Supply Current	I <sub>D</sub>	-	257	394	mA	Note 1
Power Supply Inrush Current	I <sub>Inrush</sub>	-	-	1.5	A	Note3
Power Consumption	P <sub>D</sub>	-	0.85	1.3	W	Note 1
	P <sub>BL</sub>	-	-	2.2	W	Note 2
	P <sub>total</sub>	-	3.05	3.5	W	Max@R/G/B

Notes :

1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for 3.3V at 25 °C.

- a) Typ : Mosaic pattern 5\*8
- b) Max : R/G/B patterns



Figure 3. Power Measure Patterns

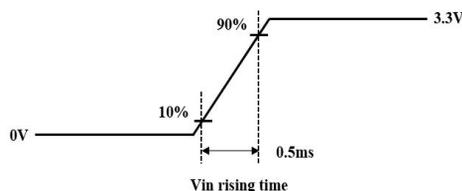


Figure 4. Inrush Measure Condition

- 2. Calculated value for reference (V<sub>L</sub>ED × I<sub>L</sub>ED)
- 3. Measure condition (Figure 4)
- 4. Input voltage range:3.0~3.6V.Test condition: Oscilloscope bandwidth 20MHz, AC coupling

## 3.2 Backlight Unit

&lt; Table 4. LED Driving Guideline Specifications &gt;

Ta=25+/-2°C

Parameter		Min.	Typ.	Max.	Unit	Remarks
LED Forward Voltage		$V_F$	-	2.9	V	-
LED Forward Current		$I_F$	19.7	-	mA	
LED Power Consumption		$P_{LED}$		2.2	W	Note 1
LED Life-Time		N/A	15,000	-	Hour	$I_F = 19.7mA$
Power Supply Voltage for LED Driver		$V_{LED}$	5	12	V	
Power Supply Voltage for LED Driver Inrush		$I_{LED}$ inrush	-	1.5	A	Note 4
EN Control Level	Backlight On		2.0	3.6	V	
	Backlight Off		0	0.6	V	
PWM Control Level	High Level		2.0	3.6	V	
	Low Level		0	0.6	V	
PWM Control Frequency		$F_{PWM}$	200	-	2,000	Hz
Duty Ratio		-	5	-	100	%

Notes :

1. Power supply voltage 2V for LED driver.

Calculator value for reference  $I_F \times V_F \times 32 / \text{driver efficiency} = P_{LED}$ 

2. The LED life-time define as the estimated time to 50% degradation of initial luminous.

3. Measure condition (Figure 5)

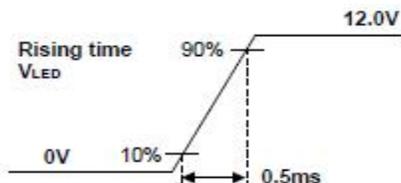


Figure 5. Inrush Measure Condition

**3.3 LED structure**

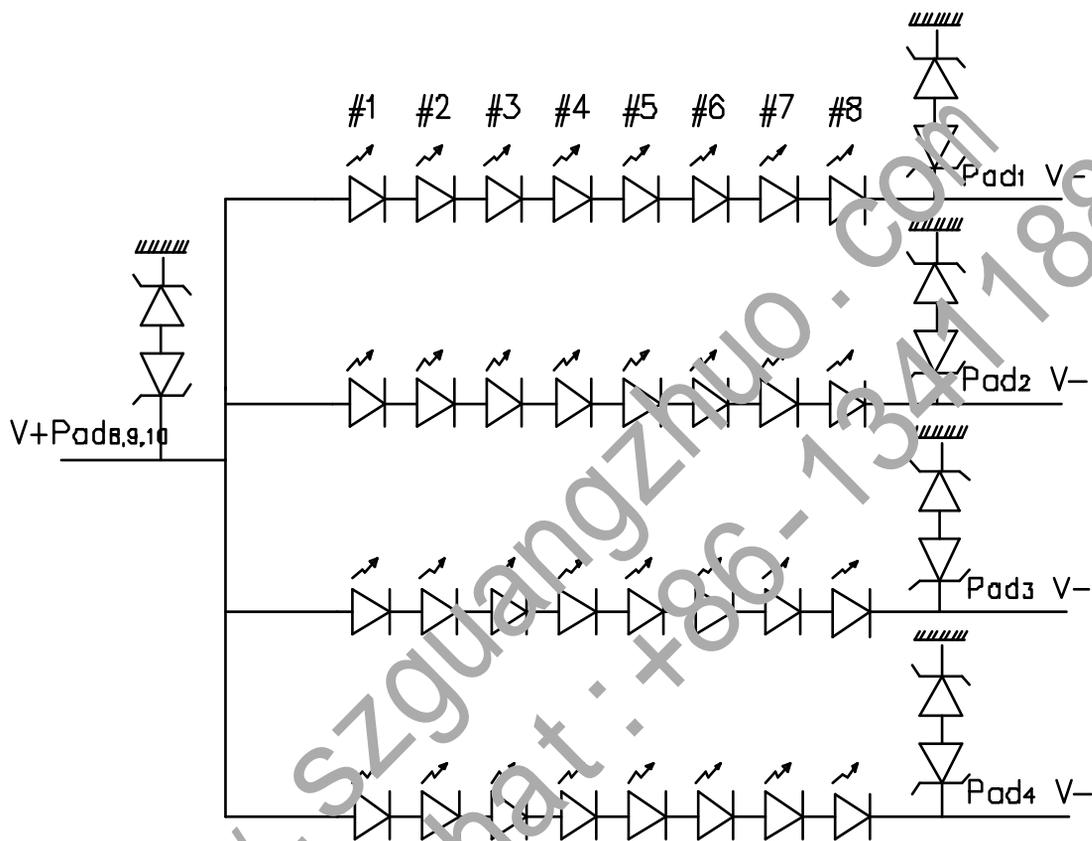


Figure 6. LED Structure

## 4.0 OPTICAL SPECIFICATION

### 4.1 Overview

The test of optical specifications shall be measured in a dark room (ambient luminance  $\leq 1$  lux and temperature =  $25 \pm 2^\circ\text{C}$ ) with the equipment of luminance meter system (PR730&PR810) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of  $\theta$  and  $\phi$  equal to  $0^\circ$ . We refer to  $\theta=0$  ( $=\theta_3$ ) as the 3 o'clock direction (the "right"),  $\theta=90$  ( $=\theta_{12}$ ) as the 12 o'clock direction ("upward"),  $\theta=180$  ( $=\theta_9$ ) as the 9 o'clock direction ("left") and  $\theta=270$  ( $=\theta_6$ ) as the 6 o'clock direction ("bottom"). While scanning  $\theta$  and/or  $\phi$ , the center of the measuring spot on the display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement. VDD shall be  $3.3 \pm 0.3\text{V}$  at  $25^\circ\text{C}$ . Optimum viewing angle direction is 6 o'clock.

### 4.2 Optical Specifications

<Table 5. Optical Specifications>

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle Range	Horizontal	$\theta_3$	CR=10	-	85	-	Deg.	Note 1
		$\theta_9$		-	85	-	Deg.	
	Vertical	$\theta_{12}$		-	85	-	Deg.	
		$\theta_6$		-	85	-	Deg.	
Luminance Contrast Ratio		CR	$\theta = 0^\circ$	600	700	-		Note 2
Luminance of White	5 Points	Y	$\theta = 0^\circ$ $I_{LED} = 10.7\text{mA}$	187	220	-	cd/m <sup>2</sup>	Note 3
White Luminance Uniformity	5 Points	$\Delta Y_5$		80%	-	-		Note 4
	13 Points	$\Delta Y_{13}$	65%	-	-			
White Chromaticity		$W_x$	$\theta = 0^\circ$	0.283	0.313	0.343		Note 5
		$W_y$		0.299	0.329	0.359		
Reproduction of Color	Red	$R_x$	$\theta = 0^\circ$	-0.03	0.587	+0.03		
		$R_y$			0.363			
	Green	$G_x$			0.351			
		$G_y$			0.577			
	Blue	$B_x$			0.163			
		$B_y$			0.122			
Color Gamut				40	45	-	%	
Response Time (Rising + Falling)		$T_{RT}$	$T_a = 25^\circ\text{C}$ $\theta = 0^\circ$	-	30	35	ms	Note 6
Cross Talk		CT	$\theta = 0^\circ$	-	-	2.0	%	Note 7

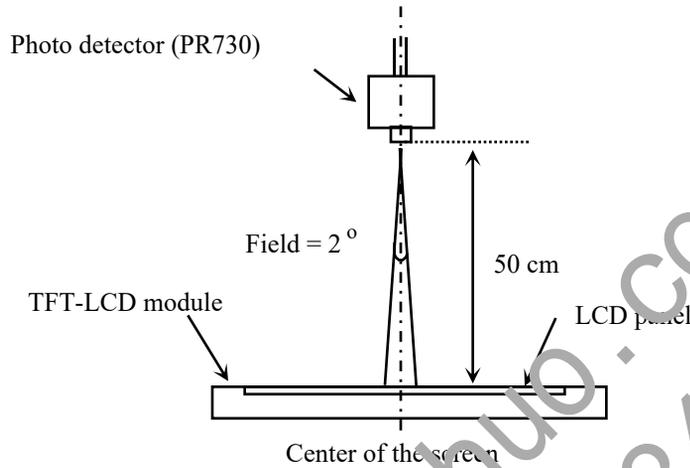
## Notes :

- Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see Figure 7).
- Contrast measurements shall be made at viewing angle of  $\Theta = 0$  and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state . (see Figure 7) Luminance Contrast Ratio (CR) is defined mathematically:

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

- Center Luminance of white is defined as luminance values of 5 point average across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in Figure 8 for a total of the measurements per display.
- The White luminance uniformity on LCD surface is then expressed as :  $\Delta Y = \text{Minimum Luminance of 5(or 13) points} / \text{Maximum Luminance of 5(or 13) points.}$ (see Figure 8 and Figure 9).
- The color chromaticity coordinates specified in Table 5 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- The electro-optical response time measurements shall be made as Figure 10 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is  $T_r$ , and 90% to 10% is  $T_f$ .
- Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark. (See Figure 11).

**4.3 Optical Measurements**



Optical characteristics measurement setup

Figure 7. Measurement Set Up

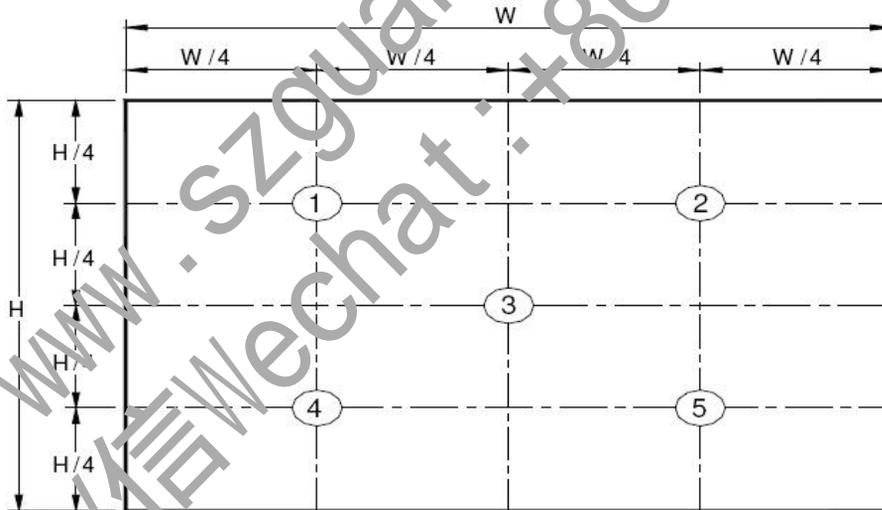


Figure 8. White Luminance and Uniformity Measurement Locations (5 points)

Center Luminance of white is defined as luminance values of center 5 points across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in Figure 8 for a total of the measurements per display.

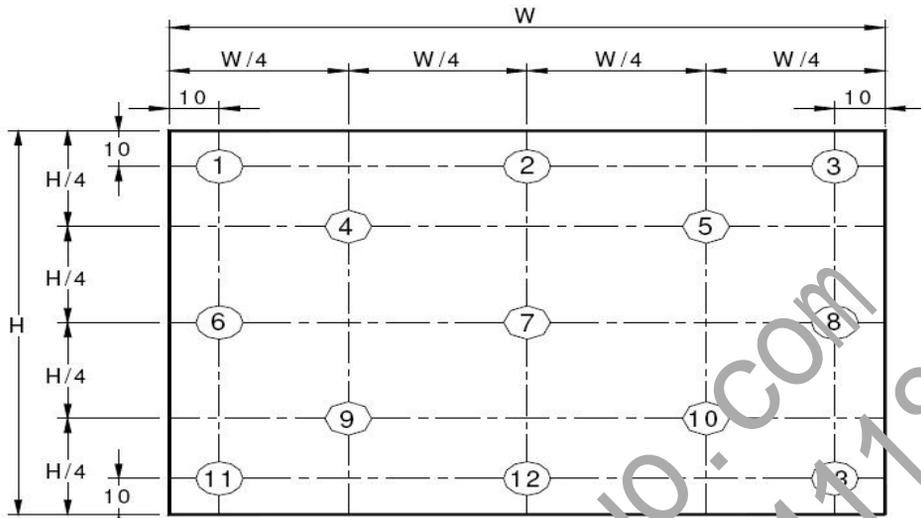


Figure 9. Uniformity Measurement Locations (13 points)

The White luminance uniformity on LCD surface is then expressed as :  $\Delta Y5$  = Minimum Luminance of five points / Maximum Luminance of five points (see Figure 8) ,  $\Delta Y13$  = Minimum Luminance of 13 points / Maximum Luminance of 13 points (see Figure 9)

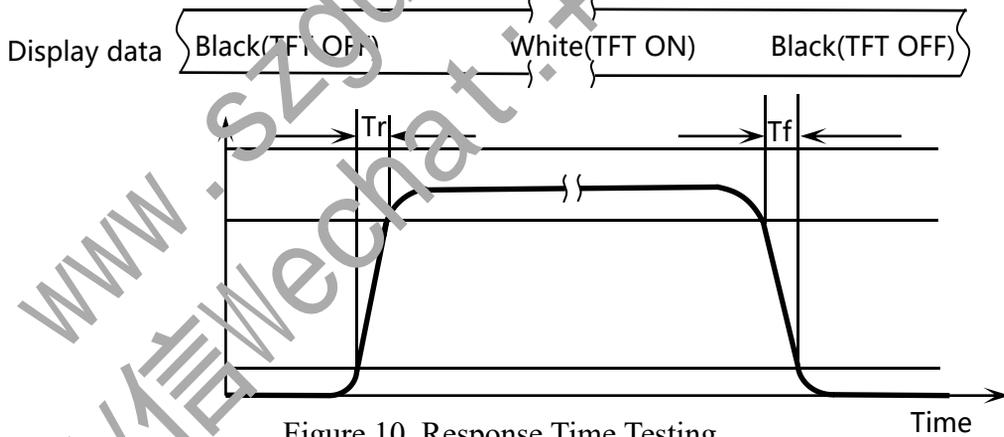
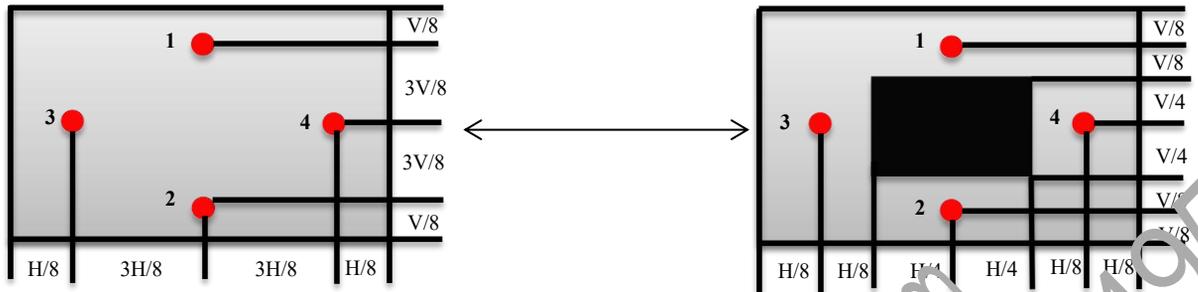


Figure 10. Response Time Testing

The electro optical response time measurements shall be made as shown in Figure 10 by switching the “data” input signal ON and OFF. Tf: The luminance to change from 90% to 10% ,Tr: The luminance to change from 10% to 90% .

The test system : PR810



$$\text{Cross-Talk (\%)} = \left| \frac{Y_B - Y_A}{Y_A} \right| \times 100$$

Figure 11. Cross Modulation Test Description

Where:

$Y_A$  = Initial luminance of measured area ( $\text{cd/m}^2$ )

$Y_B$  = Subsequent luminance of measured area ( $\text{cd/m}^2$ )

The location 1/2/3/4 measured will be exactly the same in both patterns. The test background gray is from L64 to L192. Take the largest data as the result.

Cross Talk of one area of the LCD surface by another shall be measured by comparing the luminance ( $Y_A$ ) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance ( $Y_B$ ) of that same area when any adjacent area is driven dark. (Refer to Figure 11).

The test system: PR730

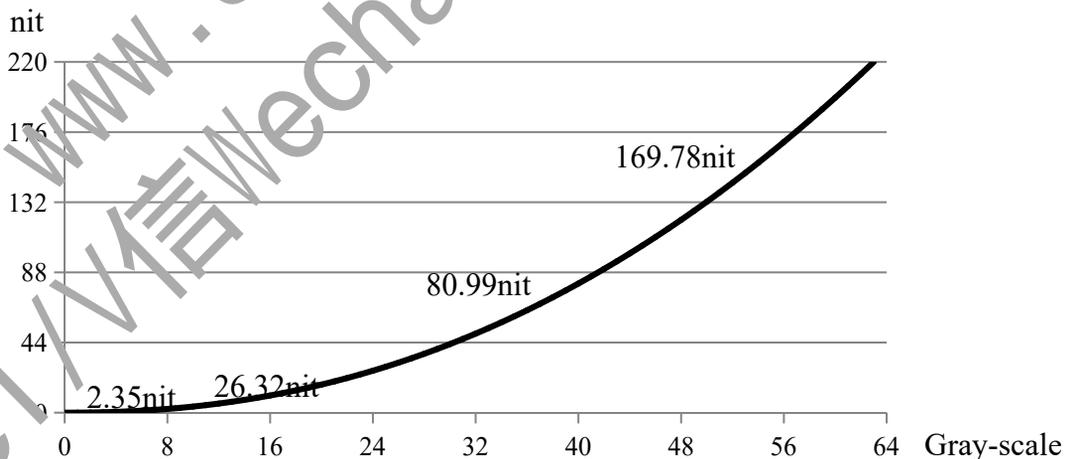


Figure 12. Brightness and Gray-scale Contrast

## 5.0 INTERFACE CONNECTION.

### 5.1 Electrical Interface Connection

The electronics interface connector is I-PEX 20455-030E-66 or Compatible.  
The connector interface pin assignments are listed in Table 6.

<Table 6. Pin Assignments for the Interface Connector>

Terminal	Symbol	Functions
Pin No.	Symbol	Description
1	CABC_Enable	CABC
2	H-GND	Ground
3	LAN1_N	Complement Signal Link_Lane1
4	LAN1_P	True Signal Link_Lane1
5	H-GND	Ground
6	LAN0_N	Complement Signal Link_Lane0
7	LAN0_P	True Signal Link_Lane0
8	H-GND	High Speed Ground
9	AUXP	True Signal Link_Auxiliary Channel
10	AUXN	Complement Signal Link_Auxiliary Channel
11	H-GND	Ground
12	LCD_VCC	Power Supply, 3.3V (typ.)
13	LCD_VCC	Power Supply, 3.3V (typ.)
14	BIST	Panel self test enable
15	H-GND	Ground
16	H-GND	Ground
17	HPD	HPD (Hot Plug Detect) Signal Pin
18	3L_GND	High Speed Ground
19	BL_GND	High Speed Ground
20	BL_GND	High Speed Ground
21	BL_GND	High Speed Ground
22	BL_EN	Backlight on/off Control pin
23	BL_PWM	Back light PWM Dimming
24	NC	No connection
25	NC	No connection
26	BL_PWR	Backlight power
27	BL_PWR	Backlight power
28	BL_PWR	Backlight power
29	BL_PWR	Backlight power
30	NC	No connection

**5.2 eDP Interface**

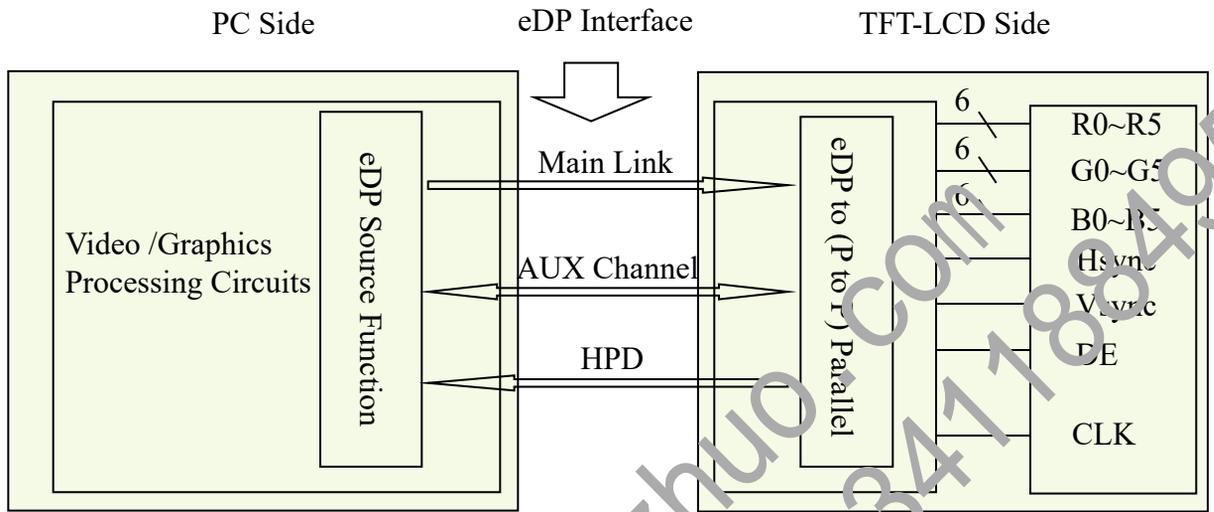


Figure 13. eDP Interface Architecture

Note:

Transmitter : Parade DP501 or equivalent.

Transmitter is not contained in module.

### 5.3 Data Input Format

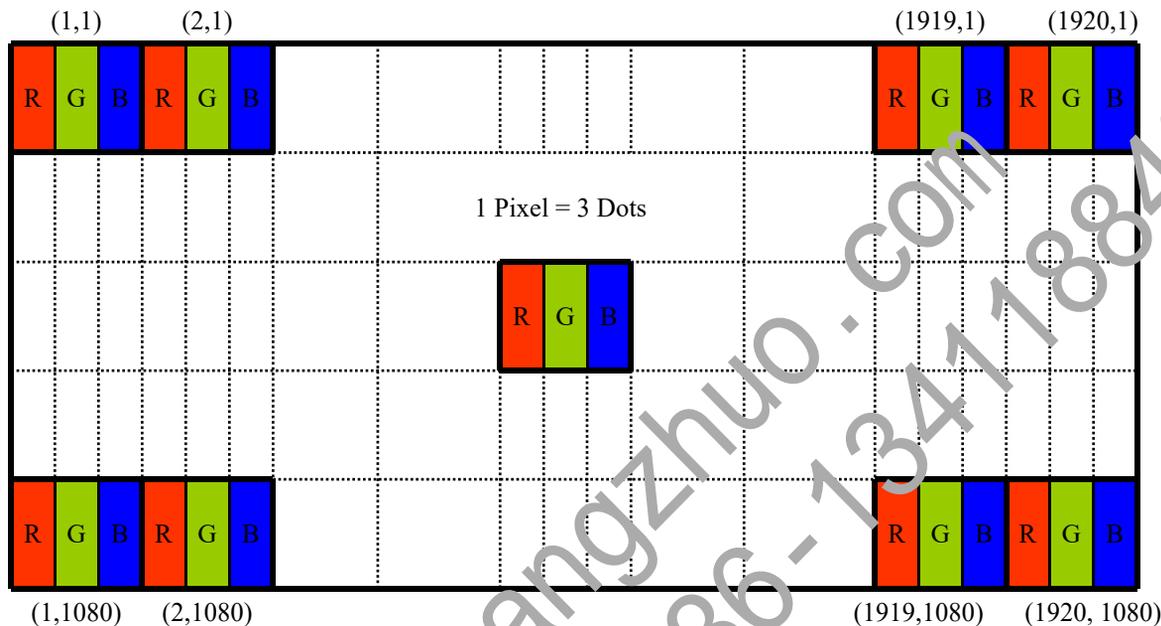


Figure 14 Display Position of Input Data (V-H)

## 5.4 Back-light & LCM Interface Connection

BLU Interface Connector: STM MSK24022P10 or Compatible.

<Table 7. Pin Assignments for the BLU Connector>

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	LED	LED cathode connection	6	GND	Ground
2	LED	LED cathode connection	7	NC	No Connection
3	LED	LED cathode connection	8	Vout	LED anode connection
4	LED	LED cathode connection	9	Vou	LED anode connection
5	NC	No Connection	10	Vout	LED anode connection

## 6.0 SIGNAL TIMING SPECIFICATION

### 6.1 The NV140FHM-N4H Is Operated By The DE Only.

< Table 8. Signal Timing Specification >

Item		Symbols	Min	Typ	Max	Unit
Clock	Frequency	1/Tc	136.3	141.4	147.8	MHz
Frame Period		Tv	1092	1100	1120	lines
			-	60	-	Hz
			-	16.67	-	ms
Vertical Display Period		Tvd	-	1080	-	lines
One line Scanning Period		Th	2080	2142	2200	clocks
Horizontal Display Period		Thd	-	1920	-	clocks

Note : The above is as optimized setting.

### 6.2 eDP Rx Interface Timing Parameter

The specification of the eDP Rx interface timing parameter is shown in Table 9.

<Table 9. eDP Main-Link RX TP4 Package Pin Parameters>

Item	Symbol	Min	Typ	Max	Unit	Remark
Spread spectrum clock (Link clock down-spreading)	ssc	0	-	0.5	%	
Differential peak-to-peak input voltage at package pins	VRX-DIFFp-p	120	-	1200	mV	
Rx input DC common mode voltage	VRX_DC_CM	0	-	2	V	
Differential termination resistance	RRX-DIFF	80	-	120	$\Omega$	
Single-ended termination resistance	RRX-SE	40	-	60	$\Omega$	
Rx short circuit current limit	IRX_SHORT	-	-	50	mA	
Intra-pair skew at Rx package pins (HBR) RX intra-pair skew tolerance at HBR	LRX_SKEW_INTRA_PAIR	-	-	60	ps	

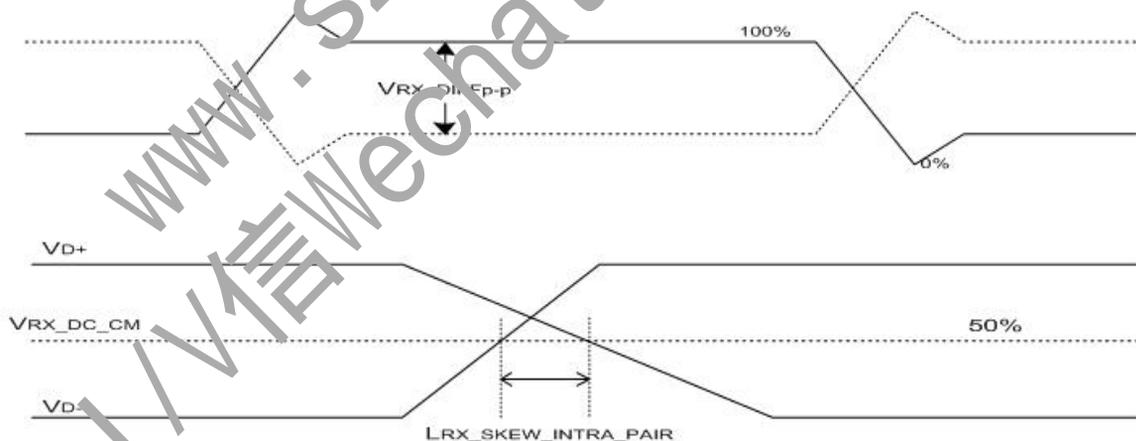


Figure 15. VRX-DIFFp-p & LRX\_SKEW\_INTRA\_PAIR

## 7.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

<Table 10. Input Signal & Basic Display Colors & Gray Scale of Colors >

	Colors & Gray scale	Data signal														
		R0 R1 R2 R3 R4 R5	G0 G1 G2 G3 G4 G5	B0 B1 B2 B3 B4 B5												
Basic colors	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0												
	Blue	0 0 0 0 0 0	0 0 0 0 0 0	1 1 1 1 1 1												
	Green	0 0 0 0 0 0	1 1 1 1 1 1	0 0 0 0 0 0												
	Light Blue	0 0 0 0 0 0	1 1 1 1 1 1	1 1 1 1 1 1												
	Red	1 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0												
	Purple	1 1 1 1 1 1	0 0 0 0 0 0	1 1 1 1 1 1												
	Yellow	1 1 1 1 1 1	1 1 1 1 1 1	0 0 0 0 0 0												
	White	1 1 1 1 1 1	1 1 1 1 1 1	1 1 1 1 1 1												
Gray scale of Red	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0												
	△	1 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0												
	Darker	0 1 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0												
	△															
	▽															
	Brighter	1 0 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0												
	▽	0 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0												
Red	1 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0													
Gray scale of Green	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0												
	△	0 0 0 0 0 0	1 0 0 0 0 0	0 0 0 0 0 0												
	Darker	0 0 0 0 0 0	0 1 0 0 0 0	0 0 0 0 0 0												
	△															
	▽															
	Brighter	0 0 0 0 0 0	1 0 1 1 1 1	0 0 0 0 0 0												
	▽	0 0 0 0 0 0	0 1 1 1 1 1	0 0 0 0 0 0												
Green	0 0 0 0 0 0	1 1 1 1 1 1	0 0 0 0 0 0													
Gray scale of Blue	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0												
	△	0 0 0 0 0 0	0 0 0 0 0 0	1 0 0 0 0 0												
	Darker	0 0 0 0 0 0	0 0 0 0 0 0	0 1 0 0 0 0												
	△															
	▽															
	Brighter	0 0 0 0 0 0	0 0 0 0 0 0	1 0 1 1 1 1												
	▽	0 0 0 0 0 0	0 0 0 0 0 0	0 1 1 1 1 1												
Blue	0 0 0 0 0 0	0 0 0 0 0 0	1 1 1 1 1 1													
Gray scale of White & Black	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0												
	△	1 0 0 0 0 0	1 0 0 0 0 0	1 0 0 0 0 0												
	Darker	0 1 0 0 0 0	0 1 0 0 0 0	0 1 0 0 0 0												
	△															
	▽															
	Brighter	1 0 1 1 1 1	1 0 1 1 1 1	1 0 1 1 1 1												
	▽	0 1 1 1 1 1	0 1 1 1 1 1	0 1 1 1 1 1												
White	1 1 1 1 1 1	1 1 1 1 1 1	1 1 1 1 1 1													

## 8.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below.

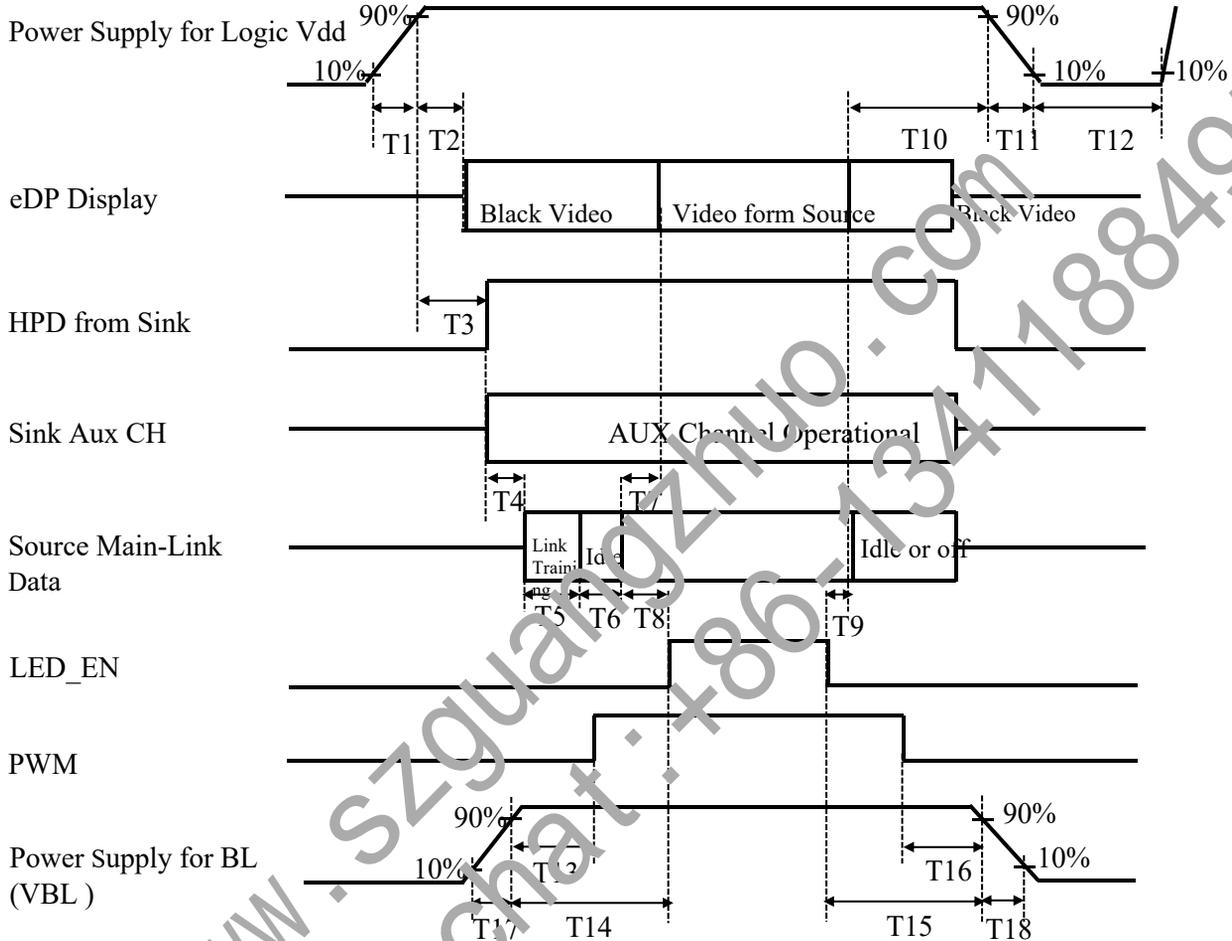


Figure 16. Power Sequence

- $0.5\text{ms} \leq T1 \leq 10\text{ms}$
- $0\text{ms} < T2 \leq 200\text{ms}$
- $0\text{ms} < T3 \leq 20\text{ms}$
- $T3+T4+T5+T6+T8 > 200\text{ms}$
- $0\text{ms} < T7 \leq 50\text{ms}$
- $50\text{ms} < T8$
- $0\text{ms} < T9$
- $0\text{ms} < T10 < 500\text{ms}$
- $0.5\text{ms} \leq T11 \leq 10\text{ms}$
- $500\text{ms} \leq T12$
- $0\text{ms} < T13$
- $0\text{ms} < T14$
- $0\text{ms} < T15$
- $0\text{ms} < T16$
- $0.5\text{ms} \leq T17$
- $0.5\text{ms} \leq T18$

- Note:
- When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
  - Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid.

### 9.0 Connector Description

Physical interface is described as for the connector on LCM.

These connectors are capable of accommodating the following signals and will be following components.

#### 9.1 TFT LCD Module

< Table 11. Signal Connector >

Connector Name /Description	For Signal Connector
Manufacturer	I-PEX or Compatible
Type/ Part Number	20455-030E-60 or Compatible
Mating Housing/ Part Number	I-PEX 20454-030T or Compatible

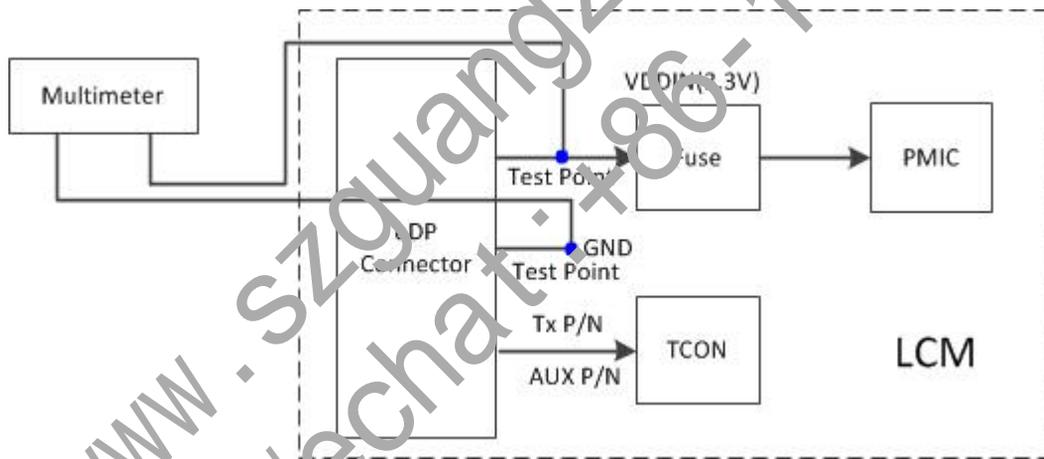
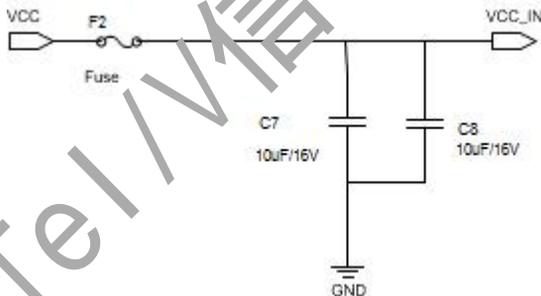


Figure 17. RC Loading test schematic diagram



Item	RC Loading	
2X30K	R	C
	7.79KΩ	36.8uF

Figure 18. VCC Loop R/C Loading Parameter

## 10.0 MECHANICAL CHARACTERISTICS

### 10.1 Dimensional Requirements

Figure 23 shows mechanical outlines for the model NV140FHM-N4H.  
Other parameters are shown in Table 12.

<Table 12. Dimensional Parameters>

Parameter	Specification	Unit
Active Area	309.31 (H) x 173.99 (V)	mm
Number of pixels	1920 (H) X 1080 (V) (1 pixel = R + G + B dot)	pixels
Pixel pitch	161.1 (H) x 161.1 (V)	um
Pixel arrangement	RGB Vertical stripe	
Display colors	262K(6bit)	
Display mode	Normally black	
Dimensional outline	(315.812±0.3)*(197.589±0.5)*(2.85±0.15)(W PCB) (315.812±0.3)*(186.069±0.5)*(2.85±0.15)(W/O PCB)	mm
Weight	285(max)	g

### 10.2 Mounting

See Figure 23.

### 10.3 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an AG coating to minimize reflection and a coating to reduce scratching.

### 10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

## 11.0 RELIABILITY TEST

The reliability test items and its conditions are shown in below.

<Table 13. Reliability Test>

No	Test Items	Conditions
1	High temperature storage test	Ta = 60°C , 60%RH, 240 hrs
2	Low temperature storage test	Ta = -20°C , 240 hrs
3	High temperature & high humidity operation test	Ta = 50°C , 80%RH, 240 hrs
4	High temperature operation test	Ta = 50°C , 60%RH, 240 hrs
5	Low temperature operation test	Ta = 0°C , 240 hrs
6	Thermal shock	Ta = -20 °C ↔ 60 °C (0.5 h), 60%±3%RH, 100 cycle
7	Vibration test (non-operating)	Ta = 25°C , 60%RH, 1.5G, 10~500Hz, Sine X,Y,Z / Sweep rate : 1 hour
8	Shock test (non-operating)	Ta = 25°C , 60%RH, 220G, Half Sine Wave 2msec ±X, ±Y, ±Z Once for each direction
9	Electro-static discharge test (operating)	Air : 150 pF, 330Ω, ±15 KV Contact : 150 pF, 330Ω, ±8 KV Ta = 25°C , 60%RH,

## 12.0 HANDLING & CAUTIONS

### (1) Cautions when taking out the module

- Pick the pouch only, when taking out module from a shipping package.

### (2) Cautions for handling the module

- As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
- As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
- As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
- Do not pull the interface connector in or out while the LCD module is operating.
- Put the module display side down on a flat horizontal plane.
- Handle connectors and cables with care.

### (3) Cautions for the operation

- When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

(4) Cautions for the atmosphere

- Dew drop atmosphere should be avoided.
- Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.

(5) Cautions for the module characteristics

- Do not apply fixed pattern data signal to the LCD module at product aging.
- Applying fixed pattern for a long time may cause image sticking.

(6) Other cautions

- Do not disassemble and/or re-assemble LCD module.
- Do not re-adjust variable resistor or switch etc.
- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping package.

## 13.0 LABEL

(1) Product Label

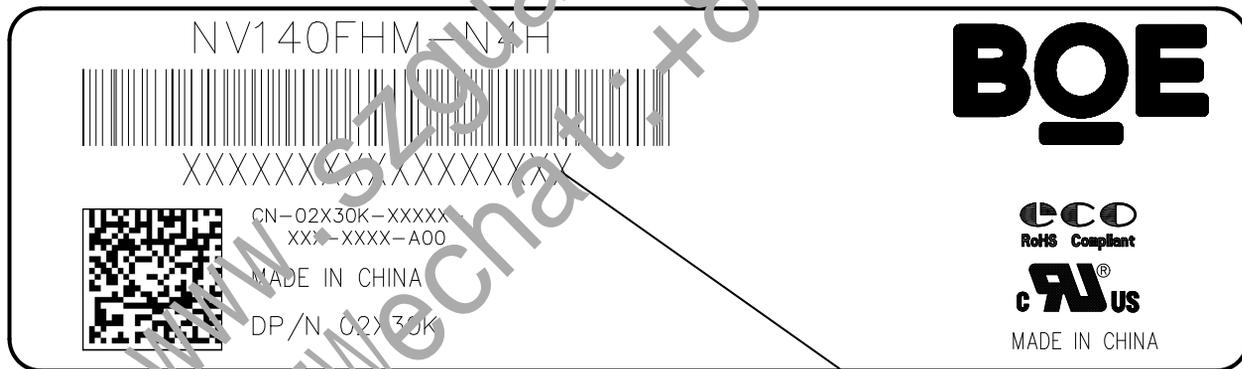


Figure 19. Product Label

Module ID Naming Rule.

<Table 14. Module ID Naming Rule>

Digit Code	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
Code	9	9	A	F	1	7	8	8	D	3	8	0	0	0	0	6	8
Description	Product Name		Product Grade	B8	Year		Month	Model Extension Code (Last 4 Digits of FG CODE)				Serial No. 00001-ZZZZZZ					

(2) High voltage caution label

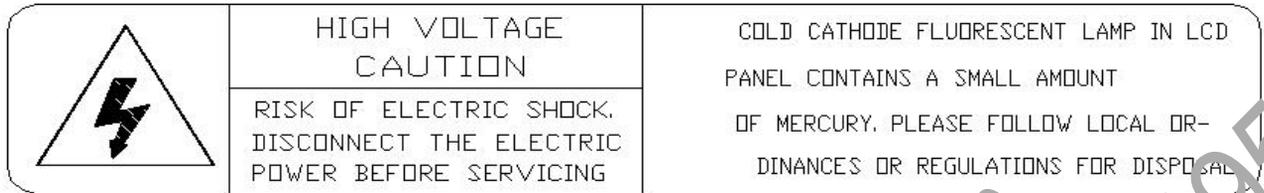


Figure 20. High Voltage Caution Label

(3) Box Label

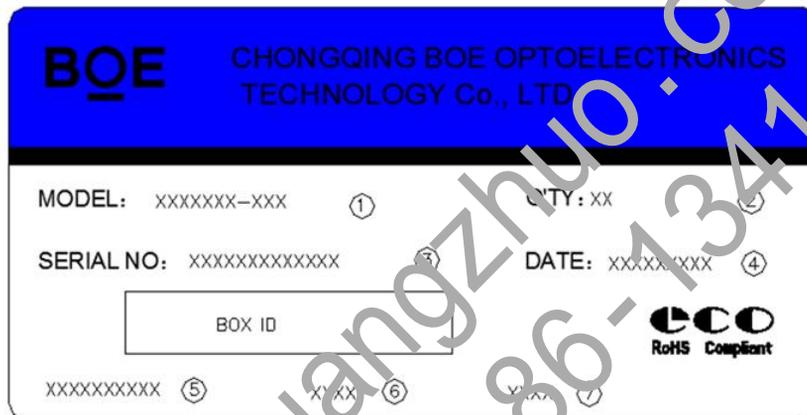


Figure 21. Box Label

Serial number marked part needs to print, show as follows:

1. FG-CODE(Before (2 bit))
2. Product quantity
3. Box ID
4. Date
5. The client section material number(The client)---02X30K
6. FG-Code After four
7. The supplier code ---BOC00

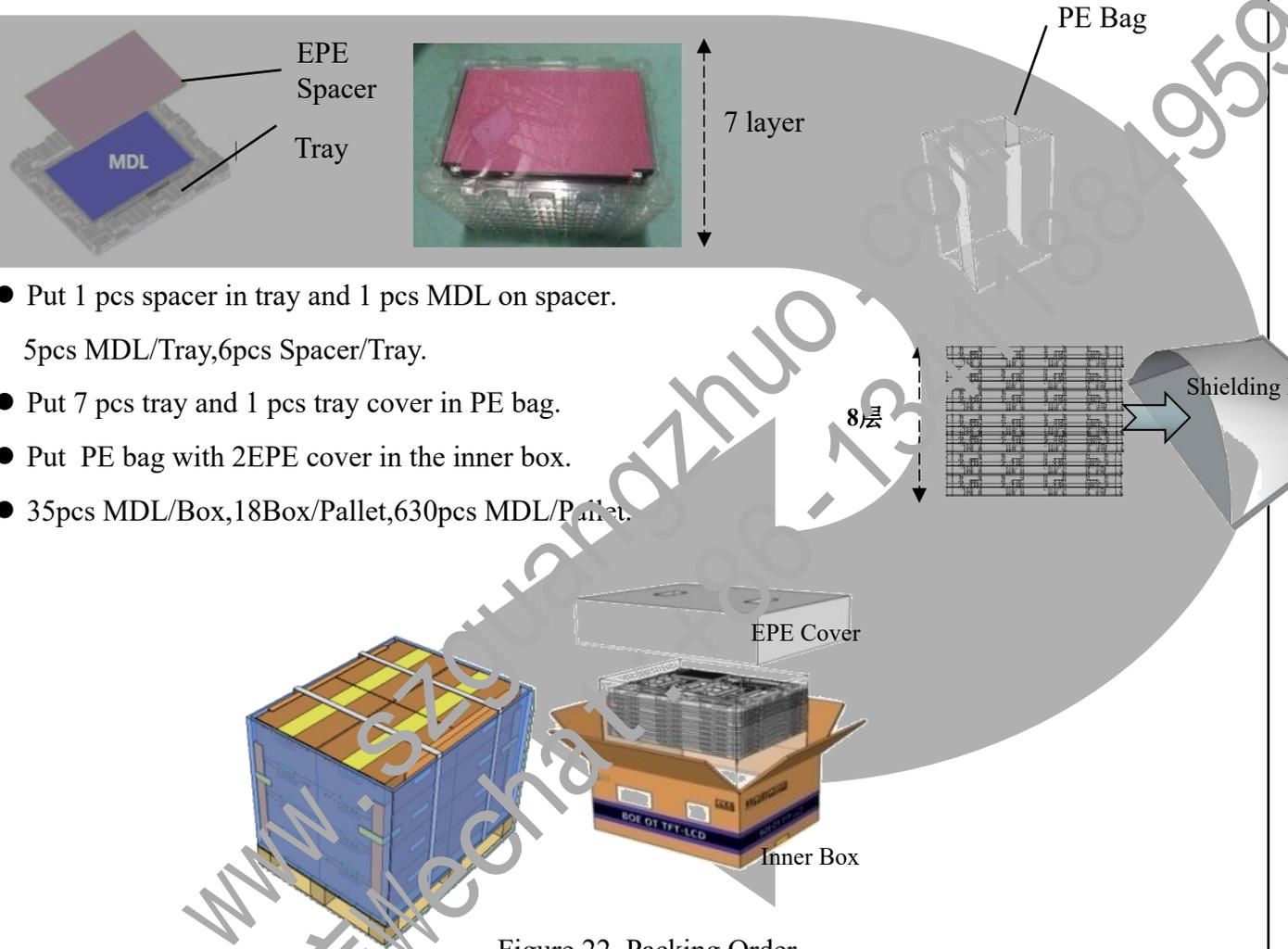
Total Size:100×50mm

<Table 15. Box Label Naming Rule >

Digit Code	1	2	3	4	5	6	7	8	9	10	11	12	13
Code	B	9	A	F	1	7	8	N	0	0	3	2	7
Description	Product Name		Product Grade	B8	Year		Month	Revision	BOX Serial Number				

## 14.0 PACKING INFORMATION

### 14.1 Packing Order



- Put 1 pcs spacer in tray and 1 pcs MDL on spacer.  
5pcs MDL/Tray,6pcs Spacer/Tray.
- Put 7 pcs tray and 1 pcs tray cover in PE bag.
- Put PE bag with 2EPE cover in the inner box.
- 35pcs MDL/Box,18Box/Pallet,630pcs MDL/Pallet.

Figure 22. Packing Order

### 14.2 Note

- Box dimension: 480mm\*350mm\*285mm
- Package quantity in one box: 35pcs
- Total weight: 11.9kg/Box

**15.0 MECHANICAL OUTLINE DIMENSION**

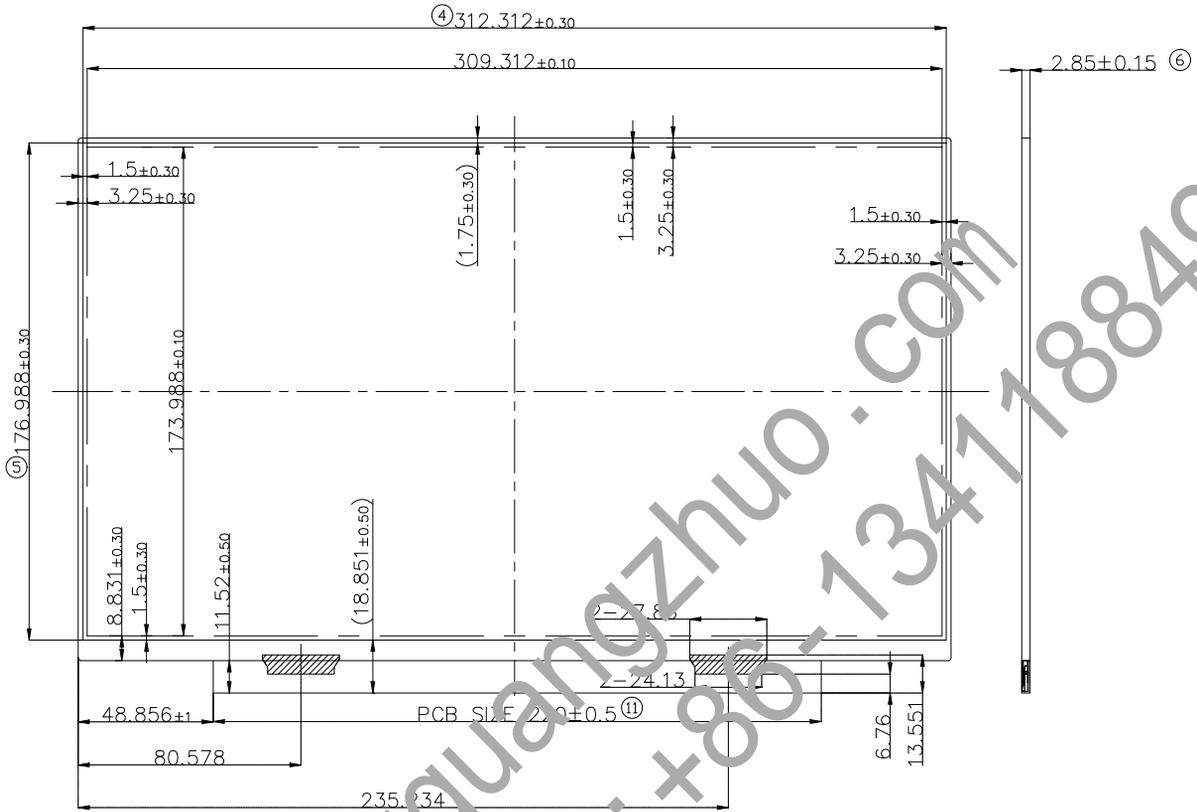


Figure 23. TFT-LCD Module Outline Dimension (Front View)

**Note:**

1. PCB side is lower than Top Polarizer, and any other PCB component is lower than Top Polarizer.
2. All sides Cell Tape is lower than Top Polarizer.
3. Curve SPEC.  $0 \leq d \leq 0.5\text{mm}$ .
4. No light leakage from all 4 corners of LCM.
5. Critical Dimensions: ①-⑫ CPK Dimensions: ①②③⑥⑪ Top POL is the highest part.
6. Coordinate Measuring Machine: ②④⑤⑦⑧⑨⑩  
Caliper: ①③⑥  
Feeler Gauge: ⑫
7. Unspecified tolerances  $\pm 0.3$ .
8. Cell tape up bulge  $\leq 0.7\text{mm}$ (Reference)
9. Cell tape down bulge  $\leq 1.6\text{mm}$ (Reference)

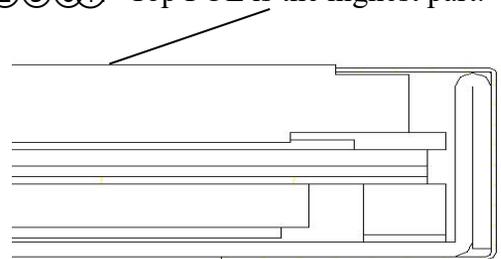


Figure 24. Highest Point Position

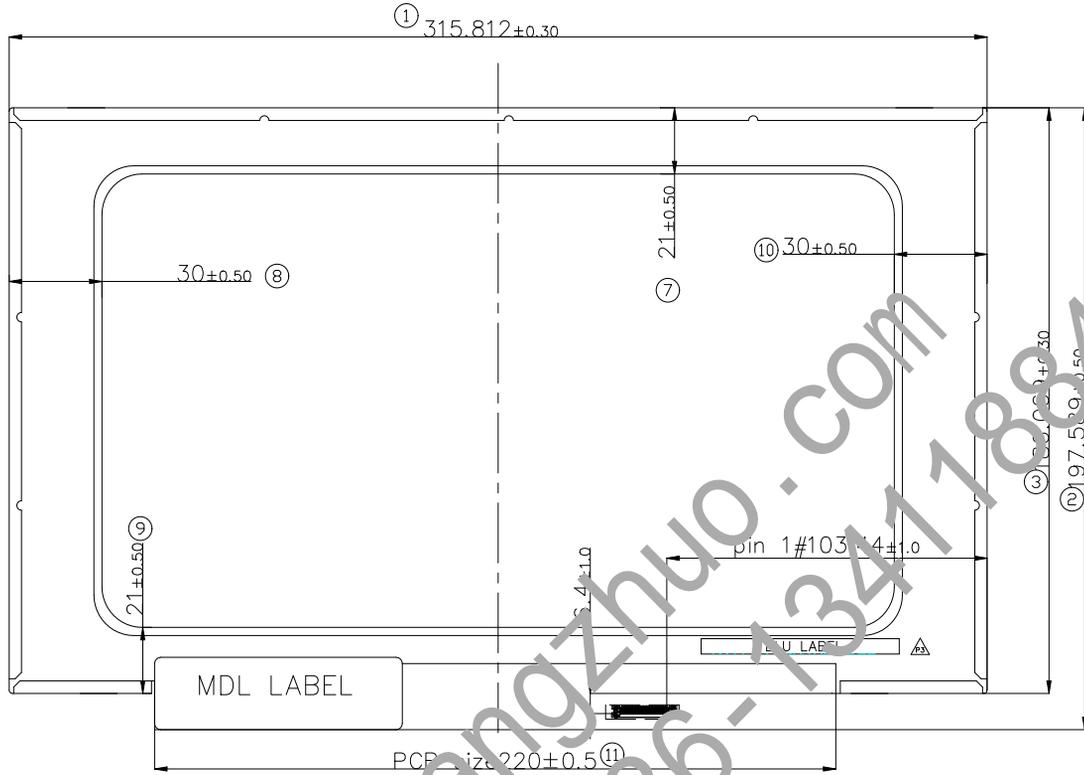


Figure 25. TFT-LCD Module Outline Dimensions (Rear view)

**Note:**

1. PCB side is lower than Top Polarizer, and any other PCB component is lower than Top Polarizer.
2. All sides Cell Tape is lower than Top Polarizer.
3. Curve SPEC:  $0 \leq d \leq 0.5\text{mm}$ .
4. No light leakage from all 4 corners of LCM.
5. Critical Dimensions: ①-⑪ CPK Dimensions: ①②③⑥⑪
6. Coordinate Measuring Machine: ②④⑤⑦⑧⑨⑩  
Caliper: ①③⑥  
Feeler Gauge: ⑫
7. Unspecified tolerances  $\pm 0.3$ .
8. Cell tape up bulge  $\leq 0.7\text{mm}$ (Reference)
9. Cell tape down bulge  $\leq 1.6\text{mm}$ (Reference)

### 16.0 EDID Table

Address (HEX)	Function	Hex	Dec	Input values	Notes
00	Header	00	0	0	EDID Header
01		FF	255	255	
02		FF	255	255	
03		FF	255	255	
04		FF	255	255	
05		FF	255	255	
06		FF	255	255	
07		00	0	0	
08	ID Manufacturer Name	09	9	BOE	ID = BOE
09		E5	229		
0A	ID Product Code	2E	46	2094	ID = 2094
0B		08	8		
0C	32-bit serial No.	00	0	0	
0D		00	0	0	
0E		00	0	0	
0F		00	0	0	
10	Week of manufacture	22	34	34	
11	Year of Manufacture	1C	28	2018	Manufactured in 2018
12	EDID Structure Ver.	01	1	1	EDID Ver 1.0
13	EDID revision #	04	4	4	EDID Rev. 0.4
14	Video input definition	95	149		Video Signal Interface
15	Max H image size	1F	31	31	31cm (Approx)
16	Max V image size	11	17	17	17cm (Approx)
17	Display Gamma	78	120	2.2	Gamma curve = 2.2
18	Feature support	02	2	-	Feature Support
19	Red/Green low bits	4F	79	-	Red / Green Low Bits
1A	Blue/White low bits	D5	213	-	Blue / White Low Bits
1B	Red x high bits	96	150	0.587	Red (x) = 10010110 (0.587)
1C	Red y high bits	5D	93	0.363	Red (y) = 01011101 (0.363)
1D	Green x high bits	59	89	0.351	Green (x) = 01011001 (0.351)
1E	Green y high bits	53	83	0.577	Green (y) = 10010011 (0.577)
1F	Blue x high bits	2C	44	0.163	Blue (x) = 00101001 (0.163)
20	Blue y high bits	1F	31	0.122	Blue (y) = 00011111 (0.122)
21	White x high bits	50	80	0.313	White (x) = 01010000 (0.313)
22	White y high bits	54	84	0.329	White (y) = 01010100 (0.329)
23	Established timing 1	00	0	-	
24	Established timing 2	00	0	-	
25	Established timing 3	00	0	-	
26	Standard timing #1	01	1	-	Not Used
27		01	1	-	
28		01	1	-	
29		01	1	-	
2A		01	1	-	
2B		01	1	-	
2C		01	1	-	
2D		01	1	-	
2E	Standard timing #5	01	1	-	Not Used
2F		01	1	-	
30	Standard timing #6	01	1	-	Not Used
31		01	1	-	
32	Standard timing #7	01	1	-	Not Used
33		01	1	-	
34	Standard timing #8	01	1	-	Not Used
35		01	1	-	

Address (HEX)	Function	Hex	Dec	Input values	Notes	
36	Detailed timing/monitor descriptor #1	39	57	141.4	141.372MHz Main clock	
37		37	55			
38		80	128	1920	Hor Active = 1920	
39		DE	222	222	Hor Blanking = 222	
3A		70	112	-	4 bits of Hor. Active + 4 bits of Hor. Blanking	
3B		38	56	1080	Ver Active = 1080	
3C		14	20	20	Ver Blanking = 20	
3D		40	64	-	4 bits of Ver. Active + 4 Bits of Ver. Blanking	
3E		30	48	48	Hor Sync Offset = 48	
3F		20	32	32	H Sync Pulse Width = 32	
40		36	54	3	V sync Offset = 3 line	
41		00	0	6	V Sync Pulse width : 6 line	
42		35	53	309	Horizontal Image Size = 309 mm (Low 8 bits)	
43		AE	174	174	Vertical Image Size = 174 mm (Low 8 bits)	
44		10	16	-	4 bits of Hor Image Size + 4 bits of Ver Image Size	
45		00	0	0	Hor Border (pixels)	
46		00	0	0	Vertical Border (Lines)	
47	1A	26	-	Detailed timing Definition		
48	Detailed timing/monitor descriptor #2	2D	45	113.1	113.097 MHz Main clock	
49		2C	44			
4A		80	128	1920	Hor Active = 1920	
4B		DE	222	222	Hor Blanking = 222	
4C		70	112	-	4 bits of Hor. Active + 4 bits of Hor. Blanking	
4D		38	56	1080	Ver Active = 1080	
4E		14	20	20	Ver Blanking = 20	
4F		40	64	-	4 bits of Ver. Active + 4 bits of Ver. Blanking	
50		30	48	48	Hor Sync Offset = 48	
51		20	32	32	H Sync Pulse Width = 32	
52		36	54	3	V sync Offset = 3 line	
53		00	0	6	V Sync Pulse width : 6 line	
54		35	53	309	Horizontal Image Size = 309 mm (Low 8 bits)	
55		AE	174	174	Vertical Image Size = 174 mm (Low 8 bits)	
56		10	16	-	4 bits of Hor Image Size + 4 bits of Ver Image Size	
57		00	0	0	Hor Border (pixels)	
58		00	0	0	Vertical Border (Lines)	
59	1A	26	-	Detailed timing Definition		
5A	Detailed timing/monitor descriptor #3	00	0	-	ASCII Data Sting Tag	
5B		00	0	-		
5C		00	0	-		
5D		FE	254	-		
5E		00	0	-	Dell P/N:2X30K	
5F		32	50	2		
60		58	88	X		
61		33	51	3	BOE PN	
62		30	48	0		
63		4B	75	K		
64		80	128	10000000		EDID:A00
65		4E	78	N		
66		56	86	V		
67		31	49	1		
68		34	52	4		
69		4E	78	N		
6A		34	52	4		
6B	48	72	H			

Address (HEX)	Function	Hex	Dec	Input values	Notes	
6C	Detailed timing/monitor descriptor #4	00	0	-	Flag	
6D		00	0	-		
6E		00	0	-		
6F		00	0	-		Data Type Tag: Manufacturer Specified Data 00
70		00	0	-		Flag
71		00	0	-	Color Depth : 6bit +2 bit FRC : No Supports	
72		41	65	-	Lamps of LED Light Bars : one Configuration : no back light color adjustment Panel Illumination : WLED	
73		21	33	-	Intel SDRS : Support Intel DRS : No Support Max. Frame Rate : 65Hz Min. Frame Rate : 10Hz	
74		96	150	-	Digatle/PWM : PWM only Maximum Typical Luminance : 220	
75		00	0	-	Pixel Structure : RGB v-strip Transflective : no AG/Glossy : Anti-Glare	
76	10	16	-	Dynamic Backlight Control : DBC type 1 Color Management : NTSC		
77	00	0	-	Active Gamma Control : no support(default) Motion Blur : no support(default)		
78	00	0	-	In-Cell Scanner : no support(default) Wireless Enhancement Hardware : no support(default)		
79	0A	10	-	In-Cell Touch : no support(default) Interface : eDP Overdrive : no support(default) Interface Channels : two		
7A	01	1	-	3-D Hardware Support : no support(default) Electronic Privacy : no electronic privacy hardware control BIST Hardware support : support(default)		
7B	0A	10	-	Format : terminate with ASCII code 0Ah and pad field with ASCII code 20h		
7C	20	32	-			
7D	20	32	-			
7E	Extension flag	00	0	1	0 : 1個EDID ; N-1 : N個EDID	
7F	Checksum	D8	216	-	Checksum	