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Final Product Specification

Rev. 0

(DELL DPN : 0HN4TM)

BOE Optoelectronics Technology Co., Ltd

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1.0 GENERAL DESCRIPTION

1.1 Introduction

NV140FHM-N4F is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 14.0 inch diagonally measured active area with Full-HD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 262k (6 bit) colors and color gamut 45%. The TFT-LCD panel used for this module is a low reflection and higher color type. Therefore, this module is suitable for Notebook PC. The LED driver for back-light driving is built in this model.

All input signals are eDP1.2 interface compatible.

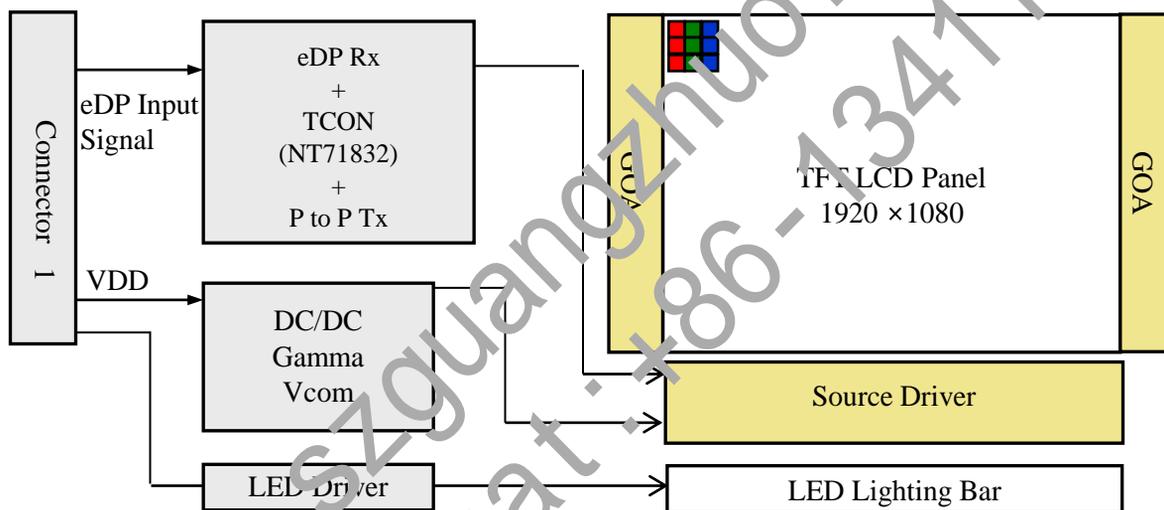


Figure 1. Drive Architecture

1.2 Features

- 2 lane eDP interface with 2.7Gbps link rates
- Thin and light weight
- 262k(6bit) color depth, color gamut 45%
- Single LED lighting bar (Bottom side/Horizontal Direction)
- Data enable signal mode
- Side mounting frame
- Green product (RoHS & Halogen free product)
- On board LED driving circuit
- Low driving voltage and low power consumption
- On board EDID chip

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1.3 Application

- Notebook PC (Wide type)

1.4 General Specification

The followings are general specifications at the model NV140FHM-N4F. (listed in Table 1)

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	309.312 (H) × 173.988 (V)	mm	
Number of pixels	1920 (H) × 1080 (V)	pixels	
Pixel pitch	161(H) × 161(V)	um	
Pixel arrangement	RGB Vertical stripe		
Display colors	262k(6bit)		
Color gamut	45%		
Display mode	Normal / Black		
Dimensional outline	355.61 ± 0.5(H) × 176.09 ± 0.5 + 1.6 (max) for FPC (V) × 2.85 ± 0.15 (W/O PCB) & 5.25(max) (W/ PCB) (T)	mm	
Weight	310(max)	g	
Surface treatment	Glare		
Surface hardness	3H		
Back-light	Bottom edge side, 1-LED lighting bar type		Note 1
Power consumption	P _D : 0.85(Max.)	W	@Mosaic
	P _{BL} : 1.95(Max.)	W	
	P _{Total} : 2.8(Max.)	W	@Mosaic

Notes : 1. LED Lighting Bar (32*LED Array)

2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings >

Ta=25+/-2°C

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	-0.3	4.0	V	Note 1
Logic Supply Voltage	V _{IN}	V _{SS} -0.3	V _{DI} +0.3	V	
Operating Temperature	T _{OP}	0	+70	°C	Note 2
Storage Temperature	T _{ST}	-20	+60	°C	

Notes :

1. Permanent damage to the device may occur if maximum values are exceeded functional operation should be restricted to the condition described under normal operating conditions.

2. Temperature and relative humidity range are shown in the figure below.

95 % RH Max. (40 °C ≥ Ta) Maximum wet-bulb temperature at 39°C or less.(Ta >40°C)No condensation.

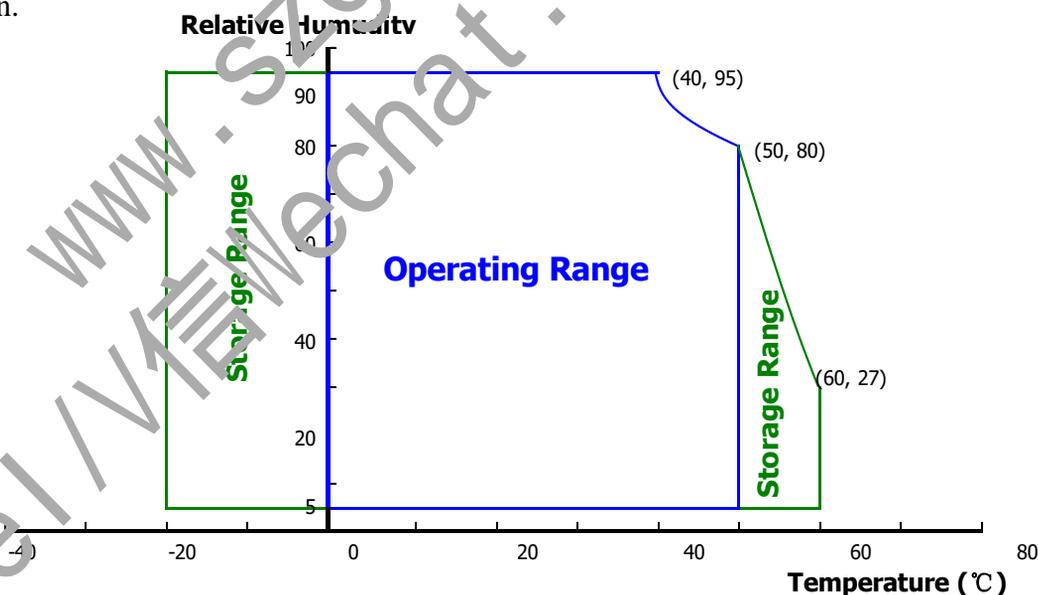


Figure 2. Temperature and Relative Humidity Range

3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 3. Electrical Specifications >

Ta=25+/-2°C

Parameter		Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	3.0	3.3	3.6	V	Note 1
Permissible Input Ripple Voltage	V _{RF}	-10% VDD	-	10% VDD		Note 4
CABC Control Level	High Level	2	-	3.6	V	
	Low Level	0	-	0.8	V	
BIST Control Level	High Level	2	-	3.6	V	
	Low Level	0	-	0.8	V	
Power Supply Current	I _{DD}	-	257	394	mA	Note 1
Power Supply Inrush Current	Inrush	-	-	1.5	A	Note3
Power Consumption	P _V	-	0.85	1.3	W	Note 1
	P _{BI}	-	-	1.95	W	Note 2
	P _{total}	-	2.8	3.25	W	@R/G/B

Notes :

1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for 3.3V at 25 °C.

a) Typ : Mosaic pattern 8*8

b) Max : R/G/B pattern



Figure 3. Power Measure Patterns

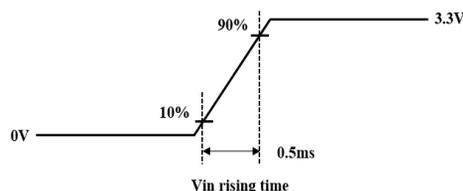


Figure 4. Inrush Measure Condition

2. Calculated value for reference (V_{LED} × I_{LED})

3. Measure condition (Figure 4)

4. Input voltage range:3.0~3.6V.Test condition: Oscilloscope bandwidth 20MHz, AC coupling.

3.2 Backlight Unit

< Table 4. LED Driving Guideline Specifications >

Ta=25+/-2°C

Parameter		Min.	Typ.	Max.	Unit	Remarks
LED Forward Voltage	V_F	-	-	2.9	V	
LED Forward Current	I_F	-	15.32	-	mA	
LED Power Consumption	P_{LED}	-	-	1.95	W	Note 1
LED Life-Time	N/A	15,000	-	-	Hour	$I_F = 15.32\text{mA}$, Note2
Power Supply Voltage for LED Driver	V_{LED}	5	12	21	V	
Power Supply Voltage for LED Driver Inrush	led inrush	-	-	15	A	Note 3
EN Control Level	Backlight On	2	-	5.0	V	
	Backlight Off	0	-	0.8	V	
PWM Control Level	High Level	2	-	5.0	V	
	Low Level	0	-	0.8	V	
PWM Control Frequency	f_{PWM}	200	-	2,000	Hz	
Duty Ratio		5	-	100	%	

Notes :

1. Power supply voltage 2V for LED driver.

Calculator value for reference $I_F \times V_F \times 32/\text{driver efficiency} = P_{LED}$

2. The LED life-time define as the estimated time to 50% degradation of initial luminous.

3. Measure condition (Figure 5).

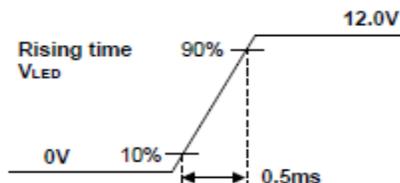


Figure 5. Inrush Measure Condition

3.3 LED Structure

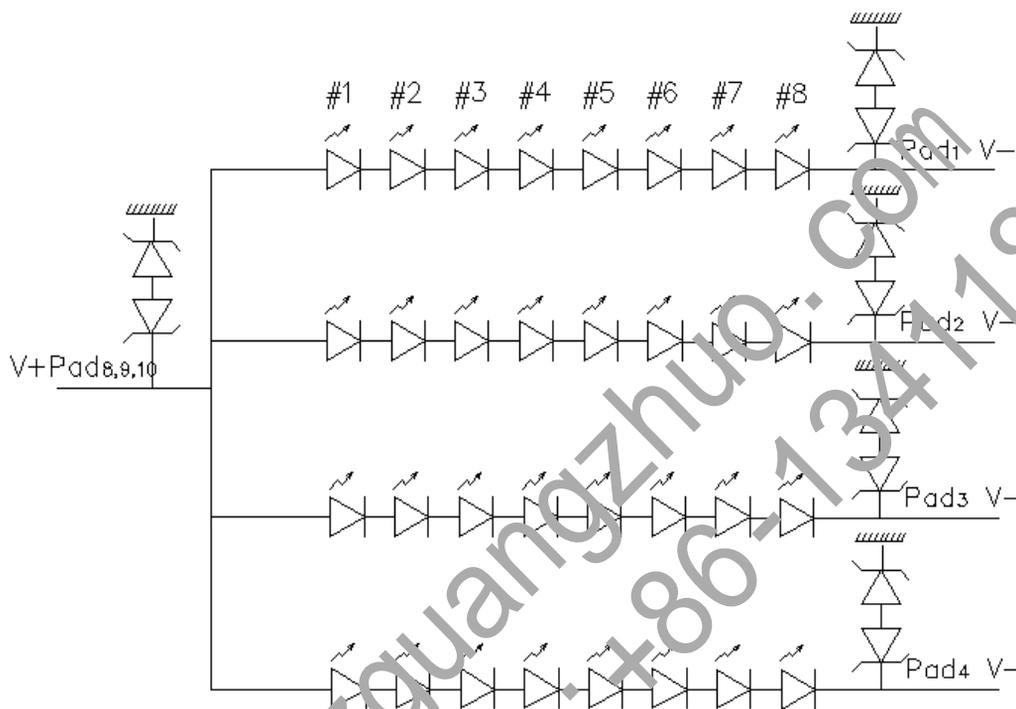


Figure 6. LED Structure

4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25 \pm 2^\circ\text{C}$) with the equipment of luminance meter system (PR730&PR810) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and ϕ equal to 0° . We refer to $\theta=0$ ($=\theta_3$) as the 3 o'clock direction (the "right"), $\theta=90$ ($=\theta_{12}$) as the 12 o'clock direction ("upward"), $\theta=180$ ($=\theta_9$) as the 9 o'clock direction ("left") and $\theta=270$ ($=\theta_6$) as the 6 o'clock direction ("bottom"). While scanning θ and/or ϕ , the center of the measuring spot on the display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement. VDD shall be $3.3 \pm 0.3\text{V}$ at 25°C . Optimum viewing angle direction is 6 o'clock.

4.2 Optical Specifications

<Table 5. Optical Specifications>

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle Range	Horizontal	θ_3	CR = 10 $\theta = 0^\circ$ ILLD = 15.32mA	80	85	89	Deg.	Note 1
		θ_9		80	85	89	Deg.	
	Vertical	θ_{12}		80	85	89	Deg.	
		θ_6		80	85	89	Deg.	
Luminance Contrast Ratio		CR	$\theta = 0^\circ$	600	700	-		Note 2
Luminance of White	5 Points	Y	$\theta = 0^\circ$ ILLD = 15.32mA	187	220	-	cd/m ²	Note 3
White Luminance Uniformity	5 Points	ΔY_5		80	-	-		Note 4
	13 Points	ΔY_{13}		65	-	-		
White Chromaticity		W_x	$\theta = 0^\circ$	0.283	0.313	0.343		Note 5
		W_y		0.299	0.329	0.359		
Reproduction of Color	Red	R_x	$\theta = 0^\circ$	Typ.-0.03	0.590	Typ.+0.03		
		R_y			0.361			
	Green	G_x			0.358			
		G_y			0.571			
	Blue	B_x			0.163			
		B_y			0.125			
Color Gamut				-	45	-	%	
Response Time (Rising + Falling)		T_{RT}	$T_a = 25^\circ\text{C}$ $\theta = 0^\circ$	-	30	35	ms	Note 6
Cross Talk		CT	$\theta = 0^\circ$	-	-	2.0	%	Note 7

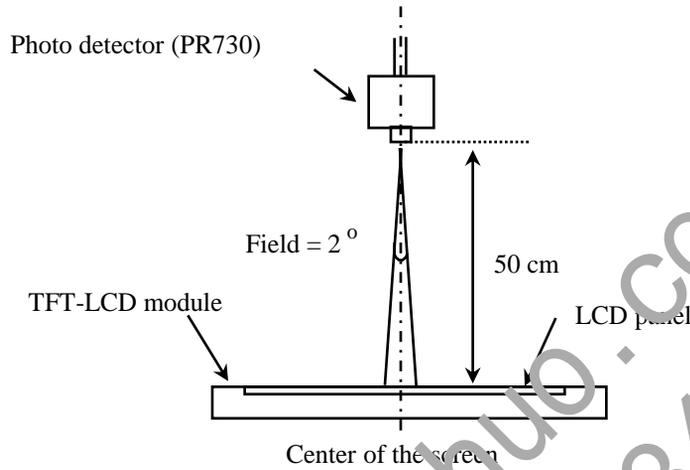
Notes :

- Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see Figure 7).
- Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (see Figure 7) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

- Center Luminance of white is defined as luminance values of 5 point average across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in Figure 8 for a total of the measurements per display.
- The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = \text{Minimum Luminance of 5(or 13) points} / \text{Maximum Luminance of 5(or 13) points}$. (see Figure 8 and Figure 9).
- The color chromaticity coordinates specified in Table 5 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- The electro-optical response time measurements shall be made as Figure 10 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_f .
- Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark. (See Figure 11).

4.3 Optical Measurements



Optical characteristics measurement setup

Figure 7. Measurement Set Up

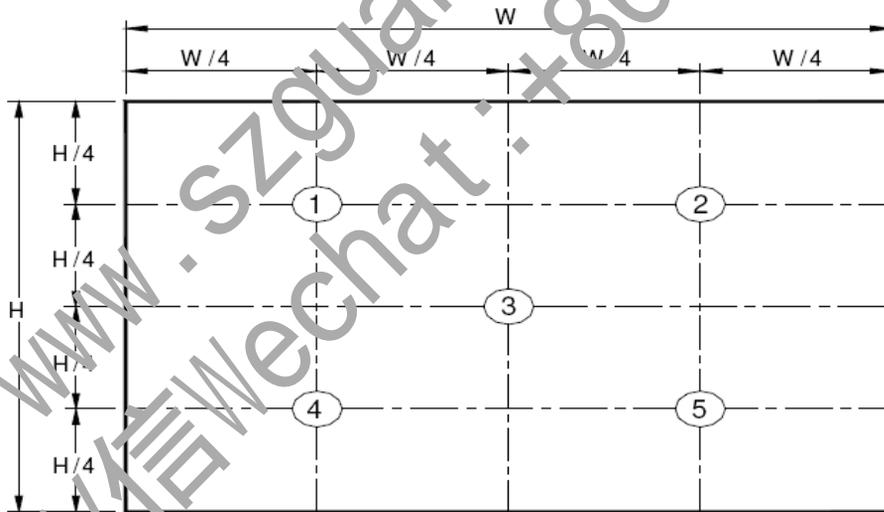


Figure 8. White Luminance and Uniformity Measurement Locations (5 points)

Center Luminance of white is defined as luminance values of center 5 points across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in Figure 7 for a total of the measurements per display.

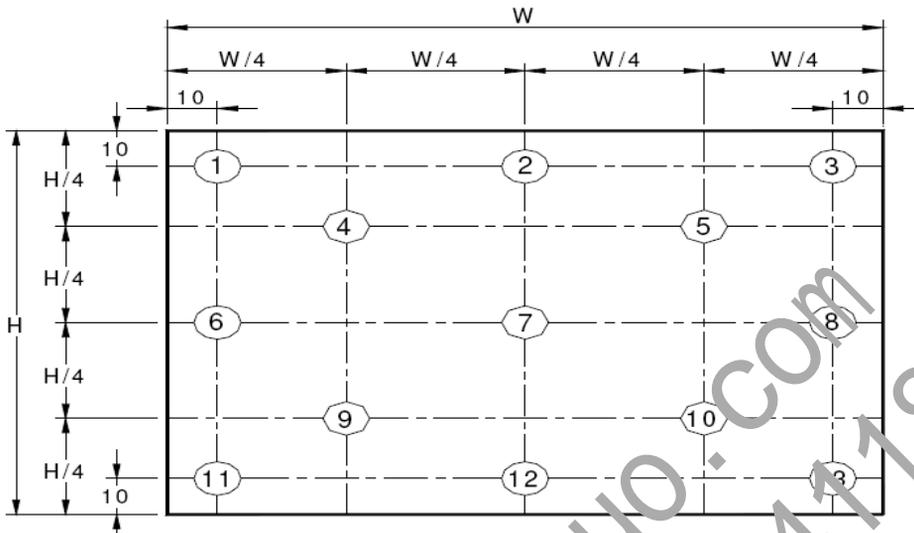


Figure 9. Uniformity Measurement Locations (13 points)

The White luminance uniformity on LCD surface is then expressed as: $\Delta Y5$ = Minimum Luminance of five points / Maximum Luminance of five points (see Figure 8), $\Delta Y13$ = Minimum Luminance of 13 points / Maximum Luminance of 13 points (see Figure 9).

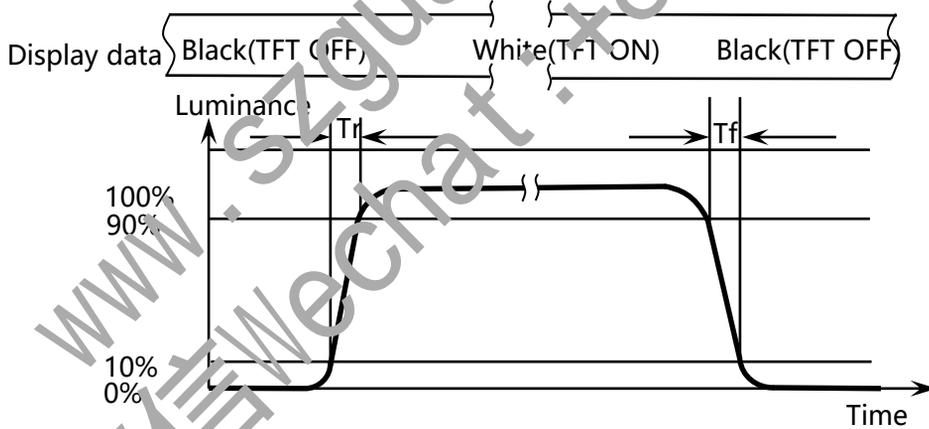
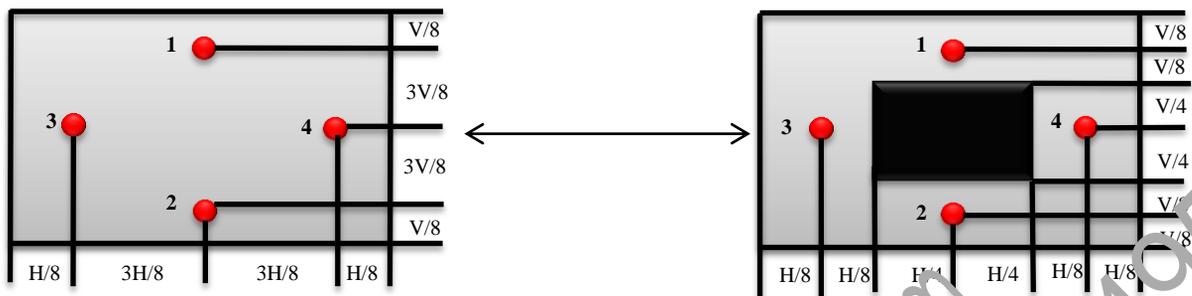


Figure 10. Response Time Testing

The electro-optical response time measurements shall be made as shown in Figure 10 by switching the “data” input signal ON and OFF. Tr: The luminance to change from 10% to 90% ,Tf: The luminance to change from 90% to 10% .

The test system : LMS PR810



$$\text{Cross Talk (\%)} = \left| \frac{Y_B - Y_A}{Y_A} \right| \times 100$$

Figure 11. Cross Talk Modulation Test Description

Where:

Y_A = Initial luminance of measured area (cd/m^2)

Y_B = Subsequent luminance of measured area (cd/m^2)

The location 1/2/3/4 measured will be exactly the same in both patterns. The test background gray is from L64 to L192. Take the largest data as the result.

Cross Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark. (Refer to Figure 11)

The test system: PR730

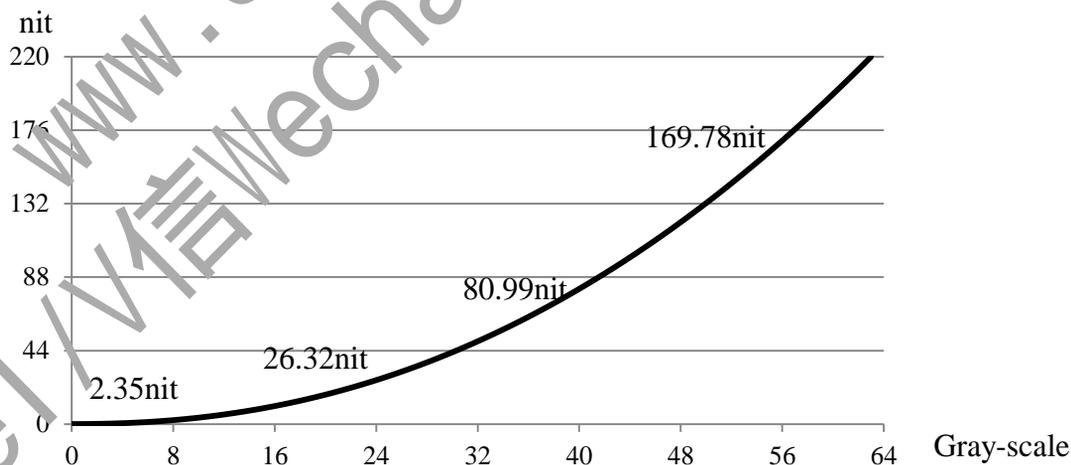


Figure 12. Brightness and Gray-scale Contrast

5.0 INTERFACE CONNECTION

5.1 Electrical Interface Connection

The electronics interface connector is STM MSAK24025P30 or Compatible.

The connector interface pin assignments are listed in Table 6.

<Table 6. Pin Assignments for the Interface Connector>

Terminal	Symbol	Functions
Pin No.	Symbol	Description
1	CABC_EN	CABC_Function Reserved
2	H_GND	Ground
3	LANE1_N	eDP RX Channel 1 Negative
4	LANE1_P	eDP RX Channel 1 Positive
5	H_GND	Ground
6	LANE0_N	eDP RX Channel 0 Negative
7	LANE0_P	eDP RX Channel 0 Positive
8	H_GND	Ground
9	AUX_CH_P	eDP AUX CH Positive
10	AUX_CH_N	eDP AUX CH Negative
11	H_GND	Ground
12	LCD_VCC	Power Supply, 3.3V (typ.)
13	LCD_VCC	Power Supply, 3.3V (typ.)
14	BIST	Panel Self Test Enable
15	H_GND	Ground
16	H_GND	Ground
17	HPD	Hot Plug Detect Output
18	BL_GND	LED Ground
19	BL_GND	LED Ground
20	BL_GND	LED Ground
21	BL_GND	LED Ground
22	BL_EN_BLE	LED Enable Pin(+3.3V Input)
23	BL_PWM	System PWM Signal Input
24	NC	No Connection
25	NC	No Connection
26	BL_POWER	LED Power Supply 5V-21V
27	BL_POWER	LED Power Supply 5V-21V
28	BL_POWER	LED Power Supply 5V-21V
29	BL_POWER	LED Power Supply 5V-21V
30	NC	No Connection

5.2 eDP Interface

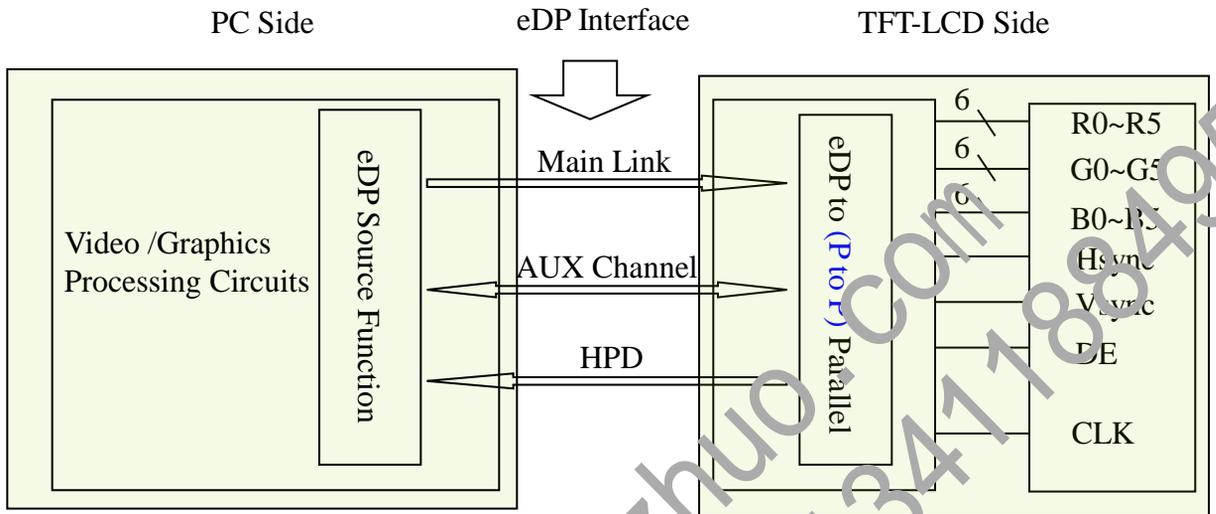


Figure 13. eDP Interface Architecture

Note:

Transmitter : Parade DP501 or equivalent.

Transmitter is not contained in module

5.3 Data Input Format

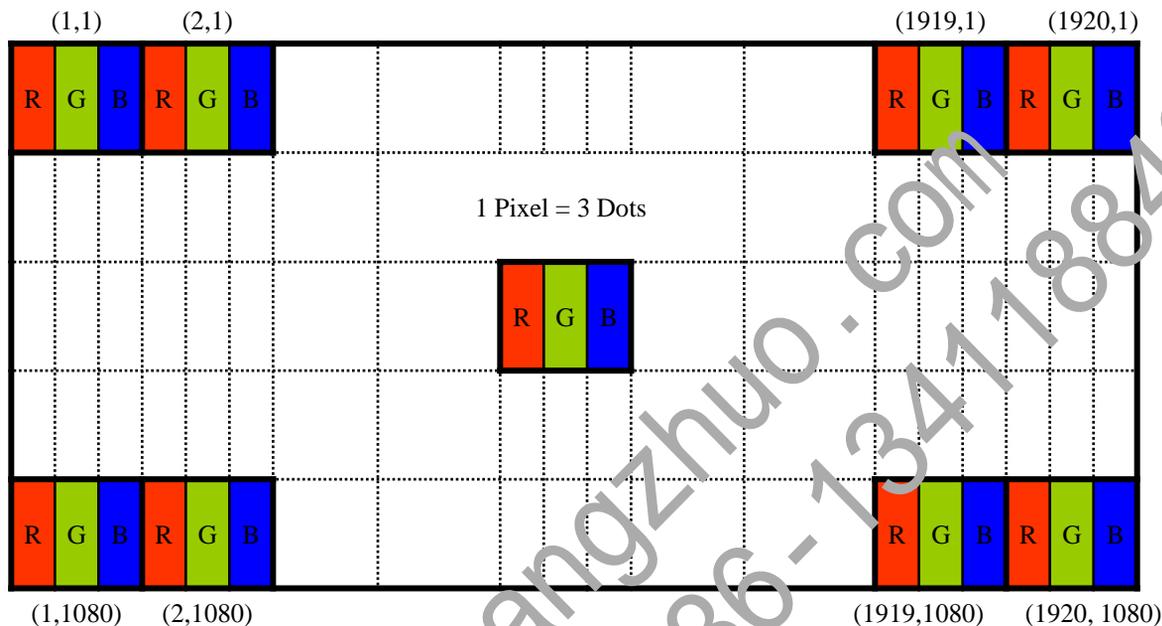


Figure 14 Display Position of Input Data (V-H)

5.4 Back-light & LCM Interface Connection

BLU Interface Connector: STM MSK24022P10 or Compatible.

<Table 7. Pin Assignments for the BLU Connector>

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	LED	LED cathode connection	6	GND	GND
2	LED	LED cathode connection	7	NC	No Connection
3	LED	LED cathode connection	8	Vout	LED anode connection
4	LED	LED cathode connection	9	Vou	LED anode connection
5	NC	No Connection	10	Vout	LED anode connection

6.0 SIGNAL TIMING SPECIFICATION

6.1 The NV140FHM-N4F is operated by the DE only

< Table 8. Signal Timing Specification >

Item		Symbols	Min	Typ	Max	Unit
Clock	Frequency	1/Tc	136.3	141.4	147.8	MHz
Frame Period		Tv	1092	1100	1120	lines
			-	60	-	Hz
			-	16.67	-	ms
Vertical Display Period		Tvd	-	1080	-	lines
One line Scanning Period		Th	2080	2142	2200	clocks
Horizontal Display Period		Thd	-	1920	-	clocks

Note : The above is as optimized setting.

6.2 eDP Rx Interface Timing Parameter

The specification of the eDP Rx interface timing parameter is shown in Table 9.

<Table 9. eDP Main-Link RX TP4 Package Pin Parameters>

Item	Symbol	Min	Typ	Max	Unit	Remark
Spread spectrum clock (Link clock down-spreading)	ssc	0	-	0.5	%	
Differential peak-to-peak input voltage at package pins	VRX-DIFFp-p	120	-	1200	mV	
Rx input DC common mode voltage	VRX_DC_CM	0	-	2	V	
Differential termination resistance	RRX-DIFF	80	-	100	Ω	
Single-ended termination resistance	RRX-SE	40	-	60	Ω	
Rx short circuit current limit	IRX_SHORT	-	-	20	mA	
Intra-pair skew at Rx package pins (HBR) RX intra-pair skew tolerance at HBR	LRX_SKEW_ INTRA_PAIR	-	-	60	ps	

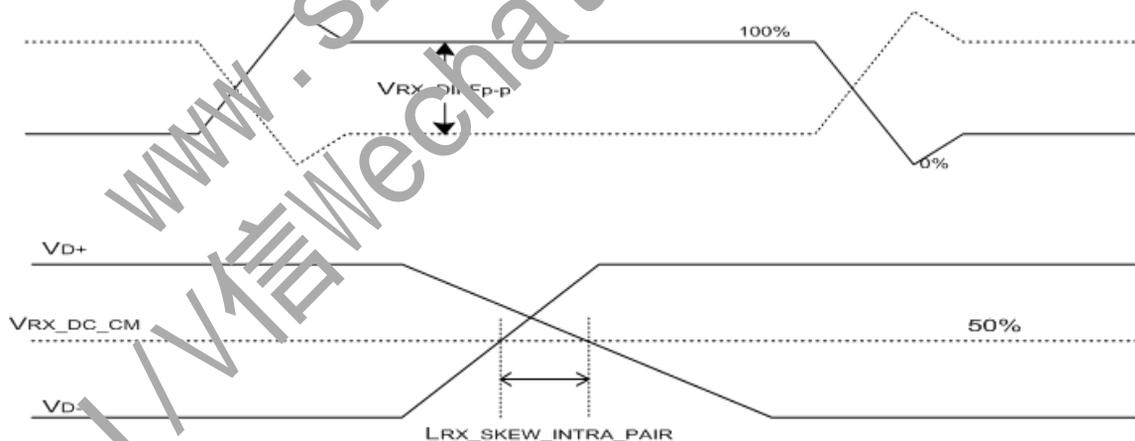


Figure 15. VRX-DIFFp-p & LRX_SKEW_INTRA_PAIR

7.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

<Table 10. Input Signal & Basic Display Colors & Gray Scale of Colors >

	Colors & Gray scale	Data signal																	
		R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	B4	B5
Basic colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Light Blue	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Purple	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray scale of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Darker	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△																		
	▽																		
	Brighter	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	
Gray scale of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	
	Darker	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	
	△																		
	▽																		
	Brighter	0	0	0	0	0	0	1	0	1	1	1	0	0	0	0	0	0	
Gray scale of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
	△																		
	▽																		
	Brighter	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	
Gray scale of White & Black	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	1	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	
	Darker	0	1	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	
	△																		
	▽																		
	Brighter	1	0	1	1	1	1	1	0	1	1	1	1	0	1	1	1	1	
White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		

8.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below.

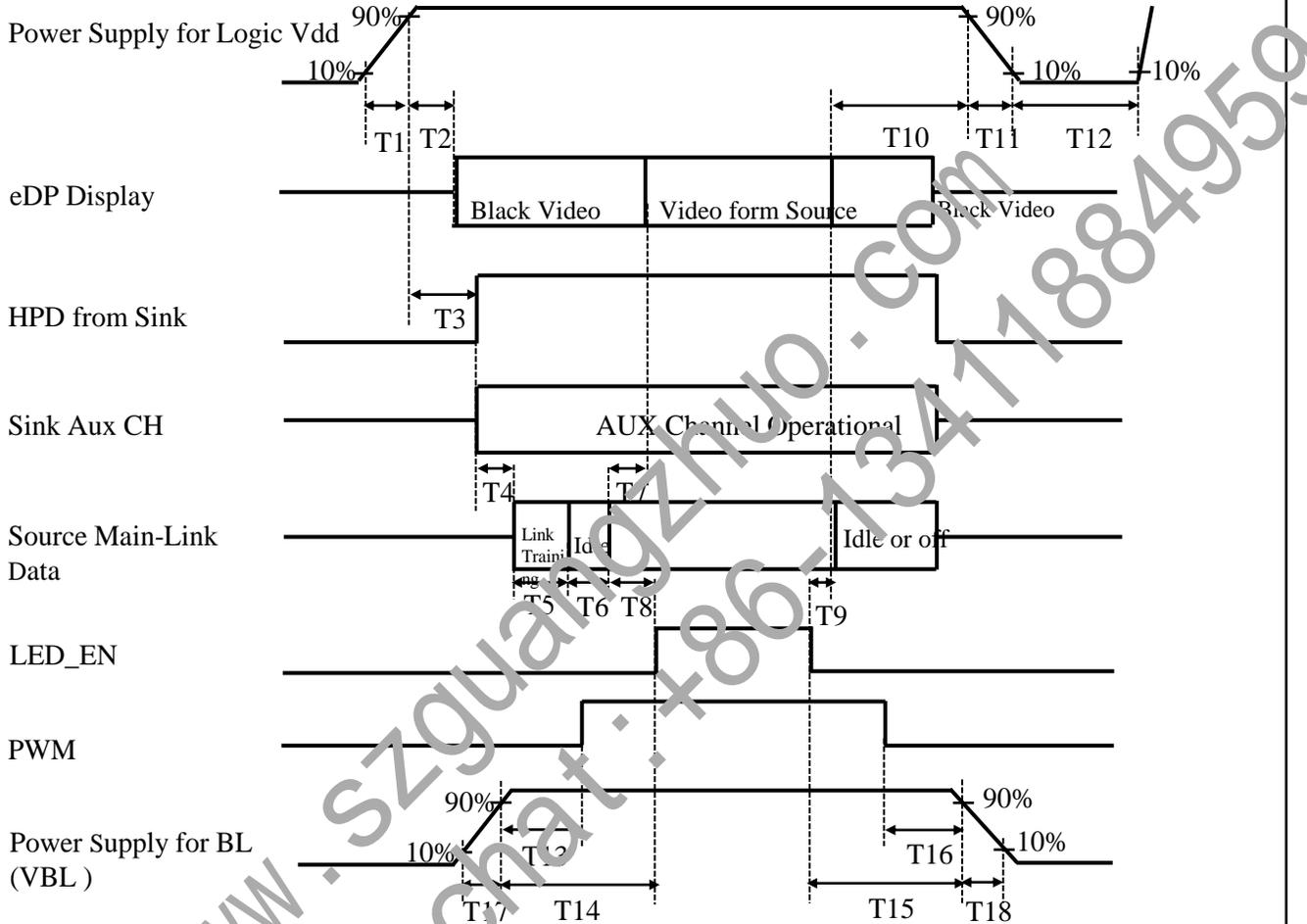


Figure 16. Power Sequence

- $0.5ms \leq T1 \leq 10ms$
- $0ms < T2 \leq 200ms$
- $0ms < T3 \leq 200ms$
- $T3+T4+T5+T6+T8 > 200ms$
- $0ms < T7 \leq 50ms$
- $50ms < T8$
- $0ms < T9$
- $0ms < T10 < 500ms$
- $0.5ms \leq T11 \leq 10ms$
- $500ms \leq T12$
- $0ms < T13$
- $0ms < T14$
- $0ms < T15$
- $0ms < T16$
- $0.5ms \leq T17$
- $0.5ms \leq T18$

Note:

- When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid.

9.0 Connector Description

Physical interface is described as for the connector on LCM.

These connectors are capable of accommodating the following signals and will be following component.

9.1 TFT LCD Module

< Table 11. Signal Connector >

Connector Name /Description	For Signal Connector
Manufacturer	STM or Compatible
Type/ Part Number	MSAK2402SP30 or Compatible
Mating Housing/ Part Number	I-PEX 20454-030T or Compatible

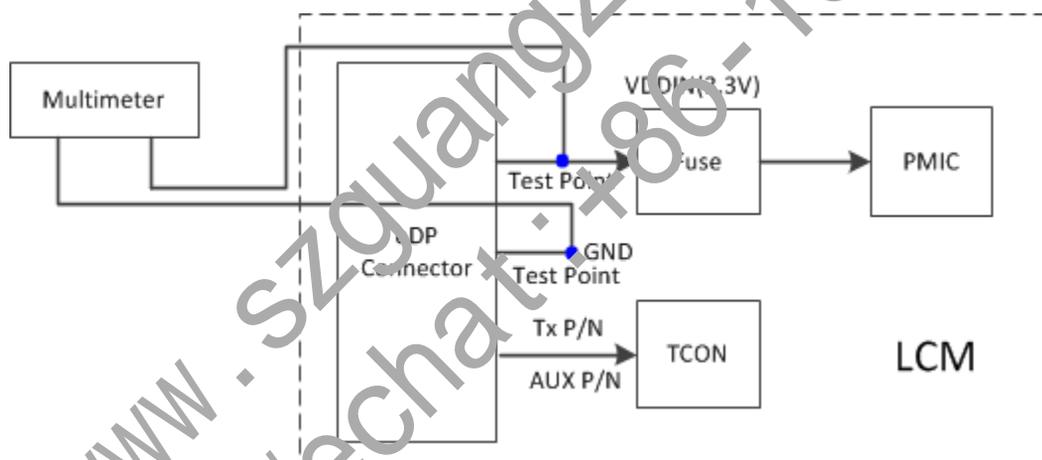
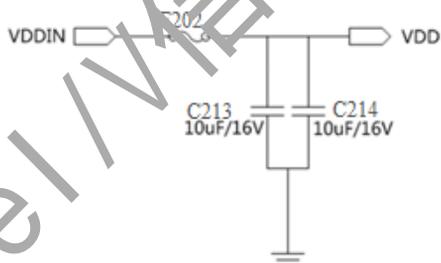


Figure 17. RC Loading Test Schematic Diagram



Item	RC Loading	
	R	C
HN4TM	7.48KΩ	34.4uF

Figure 18. VCC Loop R/C Loading Parameter

10.0 MECHANICAL CHARACTERISTICS

10.1 Dimensional Requirements

Figure 23 shows mechanical outlines for the model NV140FHM-N4F.
Other parameters are shown in Table 12.

<Table 12. Dimensional Parameters>

Parameter	Specification	Unit
Active Area	309.312 (H) × 173.988(V)	mm
Number of pixels	1920 (H) X 1080 (V) (1 pixel = R + G + B dot)	pixels
Pixel pitch	161(H) X 161(V)	um
Pixel arrangement	RGB Vertical stripe	
Display colors	262 K(6bit)	
Display mode	Normally Black	
Dimensional outline	315.81 ±0.5(H)×186.09±0.5+1.6 (max for FPC (V))×2.85±0.15 (W/C PCB)&5.25(max) (W/ PCB) (T)	mm
Weight	310 (max)	g

10.2 Mounting

See Figure 23.

10.3 Anti-Glare and Polarizer Hardness

The surface of the LCD has an Anti-Glare coating to minimize reflection and a coating to reduce scratching.

10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

11.0 RELIABILITY TEST

The reliability test items and its conditions are shown in below.

<Table 13. Reliability Test>

No	Test Items	Conditions
1	High temperature storage test	Ta = 60°C , 60%RH, 240 hrs
2	Low temperature storage test	Ta = -20°C , 240 hrs
3	High temperature & high humidity operation test	Ta = 50°C , 80%RH, 240 hrs
4	High temperature operation test	Ta = 50°C , 60%RH, 240 hrs
5	Low temperature operation test	Ta = 0°C , 240 hrs
6	Thermal shock	Ta = -20 °C ↔ 60 °C (0.5 hr), 60%±3%RH, 100 cycle
7	Vibration test (non-operating)	Ta = 25°C , 60%RH, 15G, 10~500Hz, Sine X,Y,Z / Sweep rate : 1 hour
8	Shock test (non-operating)	Ta = 25°C , 60%RH, 220G, Half Sine Wave 2ms±X,+Y,+Z Once for each direction
9	Electro-static discharge test (operating)	Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV Ta = 25°C , 60%RH,

12.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protector sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

(4) Cautions for the atmosphere

- Dew drop atmosphere should be avoided.
- Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.

(5) Cautions for the module characteristics

- Do not apply fixed pattern data signal to the LCD module at product aging.
- Applying fixed pattern for a long time may cause image sticking.

(6) Other cautions

- Do not disassemble and/or re-assemble LCD module.
- Do not re-adjust variable resistor or switch etc.
- When returning the module for repair or etc. Please pack the module not to be broken. We recommend to use the original shipping packages.

13.0 LABEL

(1) Product Label

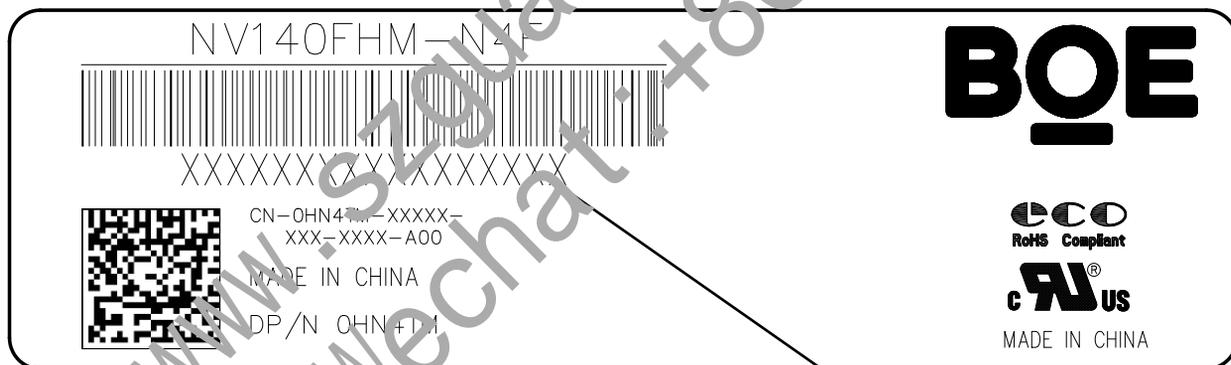


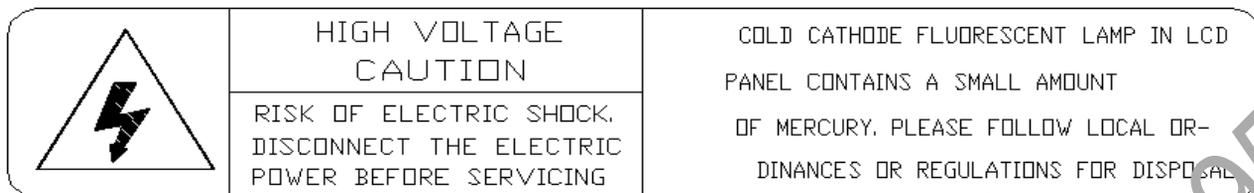
Figure 19. Product Label

Module ID Naming Rule

<Table 14. Module ID Naming Rule>

Digit	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
Code	3	9	A	F	1	7	8	8	D	3	1	0	0	0	0	6	8
Description	Product Name		Product Grade	B8	Year	Month	Model Extension Code (Last 4 Digits of FG CODE)				Serial No. 00001-ZZZZZZ						

(2) High voltage caution label



(3) Box Label

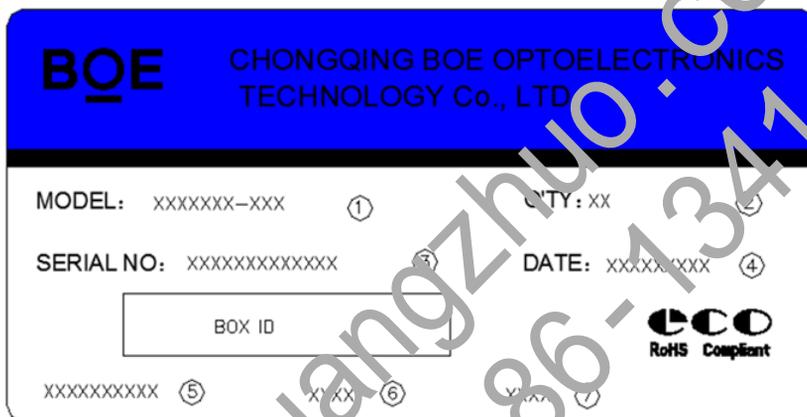


Figure 21. Box Label

Serial number marked part needs to print, show as follows:

1. FG-CODE(Before 12 bit)
2. Product quantity
3. Box ID
4. Date
5. The client section material number(The client)
6. FG-Code: After four
7. The supplier code

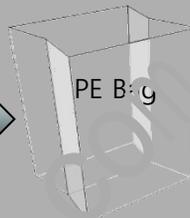
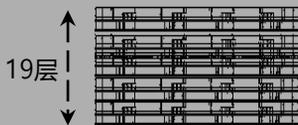
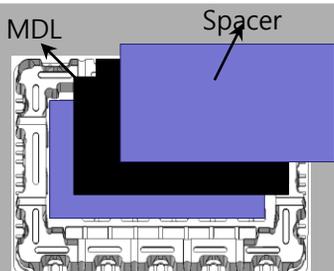
Total Size: 100×50mm

<Table 15. Box Label Naming Rule >

Digit Code	1	2	3	4	5	6	7	8	9	10	11	12	13
Code	B	9	A	F	1	7	8	N	0	0	3	2	7
Description	Product Name		Product Grade	B8	Year		Month	Revision	BOX Serial Number				

14.0 PACKING INFORMATION

14.1 Packing Order



- Put 2 pcs spacer in tray and 1 pcs MDL middle the 2 spacer. 1pcs MDL/Tray, 2pcs Spacer/Tray.
- Put 19 pcs tray in PE bag, the top of the tray is Empty.
- Put 1 PE bag with 2 EPE cover in the inner box.
- 18pcs/Box, 18Box/Pallet, 324pcs MDL/Pallet

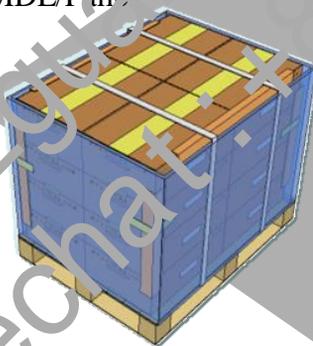


Figure 22. Packing Order

14.2 Note

- Box dimension: 467mm*351mm*270mm
- Package quantity in one box: 18pcs
- Total weight: 10.5kg/Box

15.0 MECHANICAL OUTLINE DIMENSION

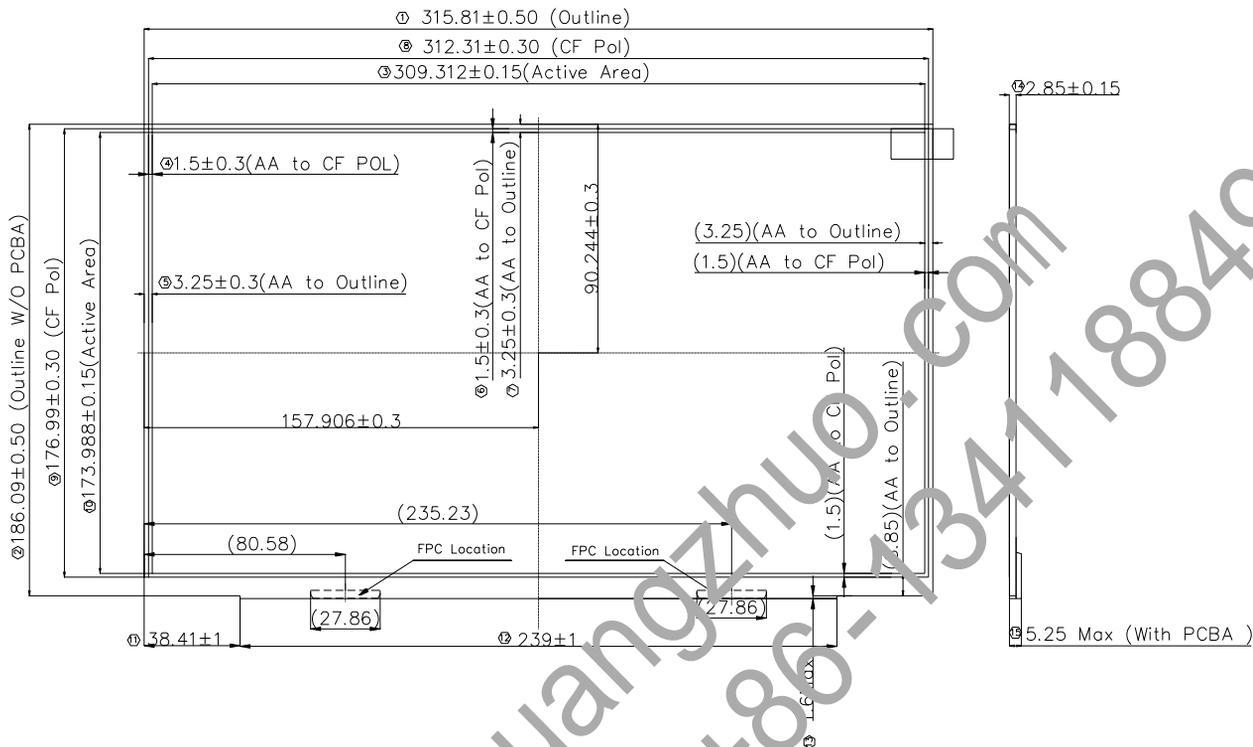


Figure 23. TFT-LCD Module Outline Dimension (Front View)

Note:

1. Top Pol is the highest surface.
2. Cure spec: $0 \leq d \leq 0.5$ (18)
3. No light leakage from all 4 corners of LCM.
4. Unspecified tolerances ± 0.3 .
5. Critical Dimensions: ① - ⑱, CPK Dimensions: ①, ②, ⑭, ⑮
6. Coordinate Measuring Machine: ③ - ⑬, ⑯, ⑰
Caliper: ①, ②, ⑭, ⑮
Feeler Gauge: ⑱
7. Cell tape bulge height in natural state
Y Direction Cell tape bulge ≤ 0.8 mm.
Z Direction front surface Cell tape bulge ≤ 1.8 mm.
Z Direction back surface Cell tape bulge ≤ 1.5 mm.

Top POL is the highest part.

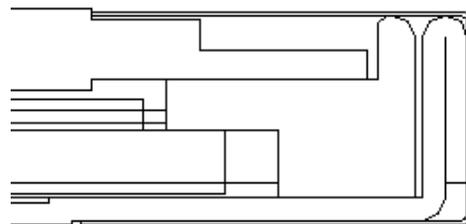


Figure 24. Highest Point Position

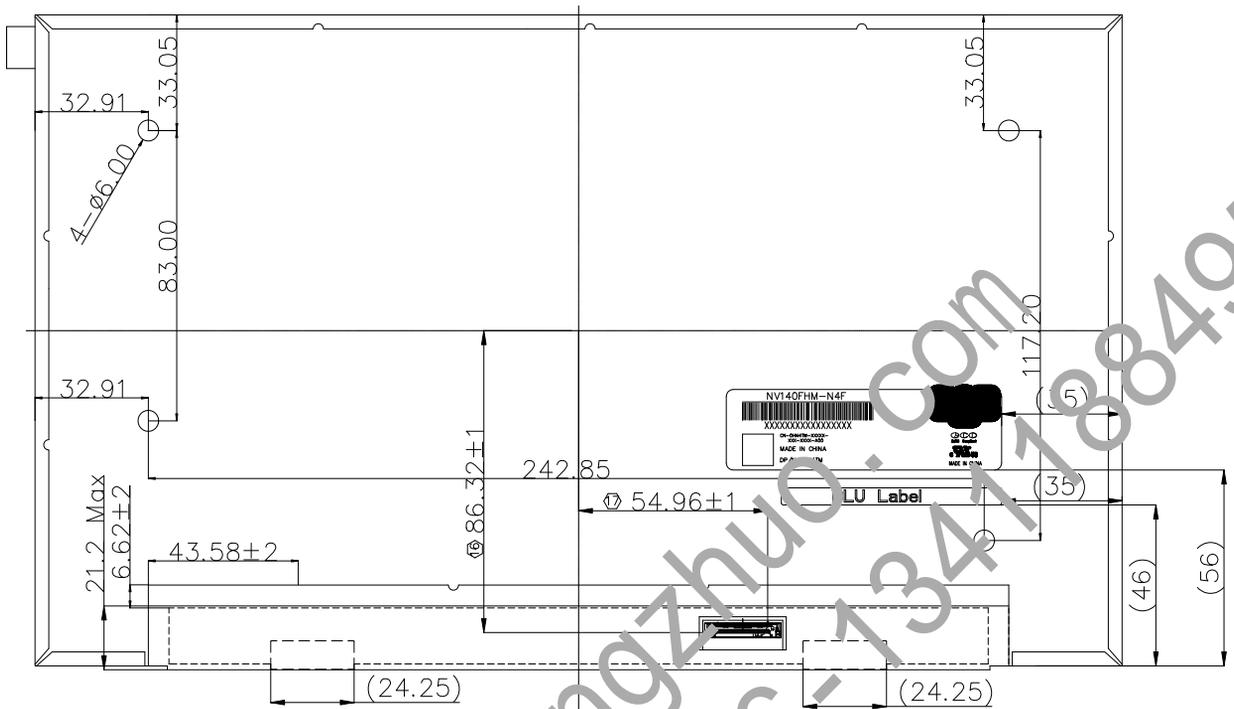


Figure 25. TFT-LCD Module Outline Dimensions (Rear view)

Note:

1. Top Pol is the highest surface.
2. Cure spec: $0 \leq d \leq 0.5$ (18)
3. No light leakage from all 4 corners of LCM.
4. Unspecified tolerances ± 0.3 .
5. Critical Dimensions: ① - ⑱, CPK Dimensions: ①, ②, ⑭, ⑮
6. Coordinate Measuring Machine: ③ - ⑬, ⑯, ⑰
Caliper: ①, ②, ⑭, ⑮
Feeler Gauge: ⑱
7. Cell tape bulge height in natural state
Y Direction Cell tape bulge ≤ 0.8 mm.
Z Direction front surface Cell tape bulge ≤ 1.8 mm.
Z Direction back surface Cell tape bulge ≤ 1.5 mm.

16.0 EDID Table

Address (HEX)	Function	Hex	Dec	crc	Input values.	Notes
00	Header	00	0		0	EDID Header
01		FF	255		255	
02		FF	255		255	
03		FF	255		255	
04		FF	255		255	
05		FF	255		255	
06		FF	255		255	
07		00	0		0	
08	ID Manufacturer Name	09	9		BOE	ID = BOE
09		E5	229			
0A	ID Product Code	BB	187		1979	ID = 1979
0B		07	7			
0C	32-bit serial No.	00	0		0	
0D		00	0		0	
0E		00	0		0	
0F		00	0		0	
10	Week of manufacture	12	18		18	
11	Year of Manufacture	1C	28		2018	Manufactured in 2018
12	EDID Structure Ver.	01	1		1	EDID Ver 1.0
13	EDID revision #	04	4		4	EDID Rev. 0.4
14	Video input definition	95	149		-	Refer to right table
15	Max H image size	17	31		31	31 cm (Approx)
16	Max V image size	11	17		17	17 cm (Approx)
17	Display Gamma	78	120		2.2	Gamma curve = 2.2
18	Feature support	02	2		-	Refer to right table
19	Red/Green low bits	18	24		-	Red / Green Low Bits
1A	Blue/White low bits	80	128		-	Blue / White Low Bits
1B	Red x high bits	97	151	604	0.590	Red (x) = 10010111 (0.59)
1C	Red y high bits	7C	92	369	0.361	Red (y) = 01011100 (0.361)
1D	Green x high bits	53	91	366	0.358	Green (x) = 01011011 (0.358)
1E	Green y high bits	92	146	584	0.571	Green (y) = 10010010 (0.571)
1F	Blue x high bits	29	41	166	0.163	Blue (x) = 00101001 (0.163)
20	Blue y high bits	20	32	128	0.125	Blue (y) = 00100000 (0.125)
21	White x high bits	50	80	320	0.313	White (x) = 01010000 (0.313)
22	White y high bits	54	84	336	0.329	White (y) = 01010100 (0.329)
23	Established timing 1	00	0		-	Refer to right table
24	Established timing 2	00	0		-	
25	Established timing 3	00	0		-	

26	Standard timing #1	01	1			Not Used
27		01	1			
28	Standard timing #2	01	1			Not Used
29		01	1			
2A	Standard timing #3	01	1			Not Used
2B		01	1			
2C	Standard timing #4	01	1			Not Used
2D		01	1			
2E	Standard timing #5	01	1			Not Used
2F		01	1			
30	Standard timing #6	01	1			Not Used
31		01	1			
32	Standard timing #7	01	1			Not Used
33		01	1			
34	Standard timing #8	01	1			Not Used
35		01	1			
36	Detailed timing/monitor descriptor #1	39	57		11.4	141.372MHz Main clock
37		37	55			
38		80	128		1920	Hor Active = 1920
39		DE	222		222	Hor Blanking = 222
3A		70	112		-	4 bits of Hor. Active + 4 bits of Hor. Blanking
3B		30	56		1080	Ver Active = 1080
3C		14	20		20	Ver Blanking = 20
3D		40	54		-	4 bits of Ver. Active + 4 bits of Ver. Blanking
3E		30	48		48	Hor Sync Offset = 48
3F		20	32		32	H Sync Pulse Width = 32
40		35	54		3	V sync Offset = 3 line
41		30	0		6	V Sync Pulse width : 6 line
42		35	53		310	Horizontal Image Size = 309.5 mm (Low 8 bits)
43		AD	173		173	Vertical Image Size = 173.2 mm (Low 8 bits)
44		10	16		-	4 bits of Hor Image Size + 4 bits of Ver Image Size
45		00	0		0	Hor Border (pixels)
46		00	0		0	Vertical Border (Lines)
47	1A	26		-	Refer to right table	

48	Detailed timing/monitor descriptor #2	2D	45		113.1	113.0976MHz Main clock
49		2C	44			
4A		80	128		1920	Hor Active = 1920
4B		DE	222		222	Hor Blanking = 222
4C		70	112		-	4 bits of Hor. Active + 4 bits of Hor. Blanking
4D		38	56		1080	Ver Active = 1080
4E		14	20		20	Ver Blanking = 20
4F		40	64		-	4 bits of Ver. Active + 4 bits of Ver. Blanking
50		30	48		48	H Sync Offset = 48
51		20	32		32	H Sync Pulse Width = 32
52		36	54		3	V sync Offset = 3 line
53		00	0		6	V Sync Pulse width: 6 line
54		35	53		310	Horizontal Image Size = 309.5 mm (Low 8 bits)
55		AD	173		173	Vertical Image Size = 173.2 mm (Low 8 bits)
56		10	16		-	4 bits of Hor Image Size + 4 bits of Ver Image Size
57		00	0		0	Hor Border (pixels)
58		00	0		0	Vertical Border (Lines)
59		1A	26		-	Refer to right above table
5A		Detailed timing/monitor descriptor #3	00	0		
5B	00		0			
5C	00		0			
5D	FE		254			
5E	00		0			Dell P/N:HN4TM
5F	48		72		H	
60	4E		78		N	
61	34		52		4	
62	54		84		T	EDID Revision:A00
63	4D		77		M	
64	50		128		10000000	
65	4E		78		N	
66	56		86		V	BOE PN
67	31		49		1	
68	34	52		4		
69	4E	78		N		
6A	34	52		4		
6B	46	70		F		

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6C	Detailed timing/monitor descriptor #4	00	0			Flag
6D		00	0			
6E		00	0			
6F		00	0			Data Type Tag: Manufacturer Specified Data 0
70		00	0			Flag
71		00	0		-	6-bit Color Depth & no FRC
72		41	65		-	WLED & single light bar & one light bar
73		21	33		-	Frame rate 40Hz~55Hz
74		96	150		-	Light Controller: PWM ?; Max Luminance 220
75		00	0		-	Front Surface: Anti-Glare ?; RGB v-stripe
76		10	16		-	with OBC
77		00	0		-	no Motion Blur & no Active Gamma
78		00	0		-	no Wireless Enhancement & no In-Cell Scanner
79		0A	10		-	2 Lane edp
7A		01	1		-	Built-In Self Test
7B		0A	10		-	Format :
7C	20	32		-	terminate with ASCII code 0Ah and pad field with ASCII code 20h	
7D	20	32		-		
7E	Extension flag	00	0	1	0 : 1個EDID ; N-1 : N个EDID	
7F	Checksum	B	183	183	-	