

TITLE : NT140WHM-N34**Product Specification****Rev. P0****HP****(45%CG/EDP1.2/Narrow Border size)****BOE Optoelectronics Technology Co., Ltd**

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REVISION HISTORY

() Preliminary Specification

(√) Final Specification

Revision No.	Page	Description of Changes	Date	Prepared
0	32	Final Release	2018.01.31	Zhou Hongru

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Contents

No.	Items	Page
1.0	General Description	4
2.0	Absolute Maximum Ratings	6
3.0	Electrical Specifications	7
4.0	Optical Specifications	10
5.0	Interface Connection	15
6.0	Signal Timing Specification	19
7.0	Input Signals, Display Colors & Gray Scale of Colors	21
8.0	Power Sequence	22
9.0	Connector Description	23
10.0	Mechanical Characteristics	24
11.0	Reliability Test	25
12.0	Handling & Cautions	25
13.0	Label	26
14.0	Packing Information	28
15.0	Mechanical Outline Dimension	29
16.0	EDID Table	31

1.0 GENERAL DESCRIPTION

1.1 Introduction

NT140WHM-N34 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 14.0inch diagonally measured active area with HD resolutions (1366 horizontal by 768 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 262k (6bit) colors and color gamut 45%. The TFT-LCD panel used for this module is a low reflection and higher color type. Therefore, this module is suitable for Notebook PC. The LED driver for back-light driving is built in this model.

All input signals are eDP1.2 interface compatible.

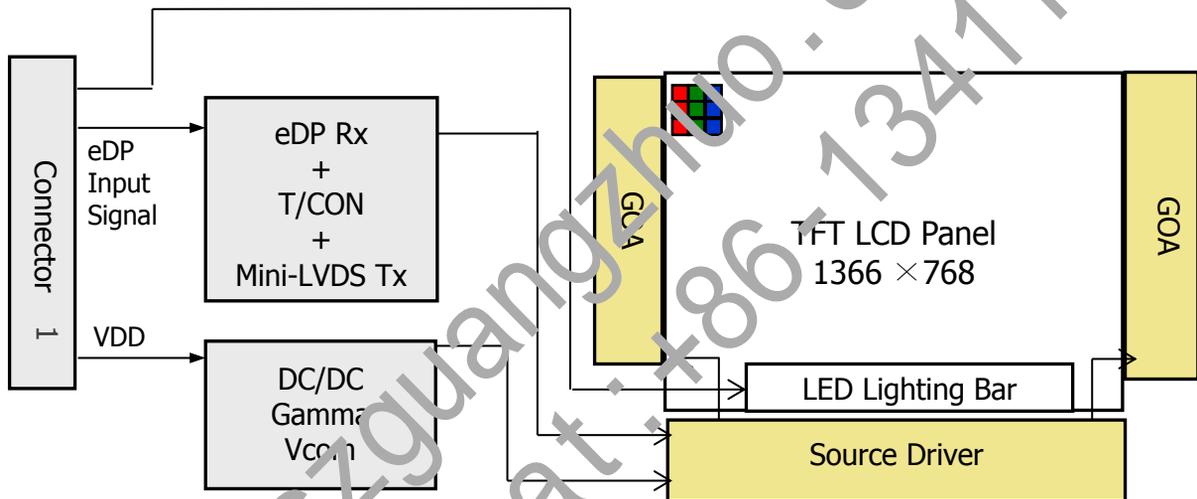


Figure 1. Drive Architecture

1.2 Features

- 2 lane eDP interface with 2.7Gbps link rates
- Thin and light weight
- 262k(6bit) color depth, color gamut 45%
- Single LED lighting bar (Bottom side/Horizontal Direction)
- Data enable signal mode
- Side mounting frame
- Green product (RoHS & Halogen free product)
- On board LED driving circuit
- Low driving voltage and low power consumption
- On board EDID chip

1.3 Application

- Notebook PC (Wide type)

1.4 General Specification

The followings are general specifications at the model NT140WHM-N34. (listed in Table 1)

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	309.4(H) × 173.95(V)	mm	
Number of pixels	1366 (H) × 768 (V)	pixels	
Pixel pitch	0.07546(H) × 0.22629(V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	262k(6bit)		
Color gamut	45%		
Display mode	Normally white		
Dimensional outline	316.2x198.05(With PCB) (Max)x.3.0(Max)	mm	
Weight	265(max)	g	
Surface treatment	Glare		
Surface hardness	3H		
Back-light	Lower Down side, 1-LED Lighting Bar type		Note 1
Power consumption	P_D : 0.7	W	@Mosaic
	P_{BL} : 2.2	W	max
	P_{TOTAL} : 3.1 for mosaic pattern , P_{TOTAL} : 3.1 for R/G/B/Black/White patterns	W	max

Notes : 1. LED Lighting Bar (32*LED Array)

2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings >

Ta=25+/-2°C

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	-0.3	4.0	V	Note 1
Logic Supply Voltage	V _{IN}	V _{SS} -0.3	V _{DD} +0.3	V	
Operating Temperature	T _{OP}	0	+30	°C	Note 2
Storage Temperature	T _{ST}	-20	+60	°C	

Notes :

1. Permanent damage to the device may occur if maximum values are exceeded functional operation should be restricted to the condition described under normal operating conditions.
2. Temperature and relative humidity range are shown in the figure below.
95 % RH Max. (40 °C ≥ Ta) Maximum wet - bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation.

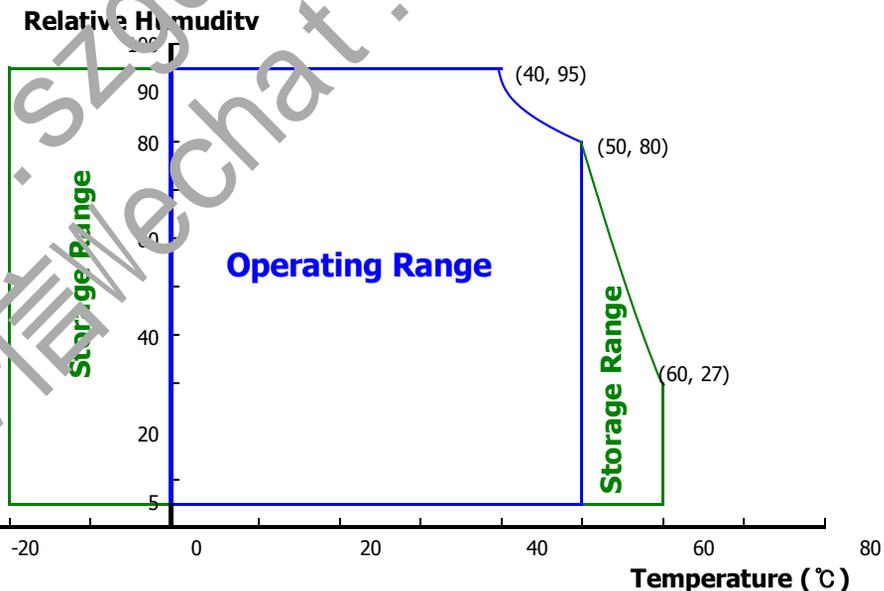


Figure 2. Temperature and Relative Humidity Range

3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 3. Electrical Specifications >

Ta=25+/-2°C

Parameter		Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	3.0	3.3	3.6	V	Note 1
Permissible Input Ripple Voltage	V _{RF}	-	-	100	mV	@ V _{DD} = 3.3V
Power Supply Current	I _{DD}	-	212	273	mA	Note 1
Power Supply Inrush Current	I _{inrush}	-	-	1.5	A	Note3
Power Consumption	P _D	-	0.7	0.9	W	Note 1
	P _{BL}	-	-	2.2	W	Note 2
	P _{total}	-	2.9	3.1	W	Note 1

Notes :

- The supply voltage is measured and specified at the interface connector of LCM.
The current draw and power consumption specified is for 3.3V at 25 °C.

- Typ : Mosaic pattern 8*8
- Max : R/G/B patterns



Figure 3. Power Measure Patterns

- Calculated value for reference (V_{LED} × I_{LED})
- Measure condition (Figure 4)

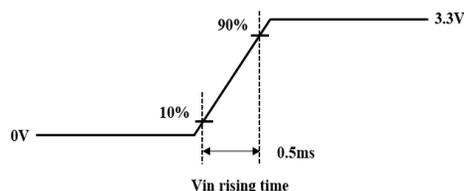


Figure 4. Inrush Measure Condition

3.2 Backlight Unit

< Table 4. LED Driving Guideline Specifications >

Ta=25+/-2°C

Parameter		Min.	Typ.	Max.	Unit	Remarks
LED Forward Voltage		V_F	-	2.9	-	V
LED Forward Current		I_F	-	17.5	-	mA
LED Power Consumption		P_{LED}	-	1.9	-	W Note 1
LED Life-Time		N/A	15,000	-	-	Hour $I_F = 17.5mA$
Power Supply Voltage for LED Driver		V_{LED}	6	12	21	V
Power Supply Voltage for LED Driver Inrush		I_{LED} inrush	-	-	1.5	A Note 4
EN Control Level	Backlight On	2.5	-	5.0	V	
	Backlight Off	0	-	0.6	V	
PWM Control Level	High Level	2.5	-	5.0	V	
	Low Level	0	-	0.6	V	
PWM Control Frequency		F_{PWM}	200	-	10,000	Hz
Duty Ratio			1	-	100	% Note 3

Notes :

1. Power supply voltage 12V for LED driver.

Calculator value for reference $I_F \times V_F \times 32 / \text{driver efficiency} = P_{LED}$

2. The LED life-time define as the estimated time to 50% degradation of initial luminous.

3. 1% duty cycle is achievable with a dimming frequency less than 1KHz.

4. Measure condition (Figure 5)

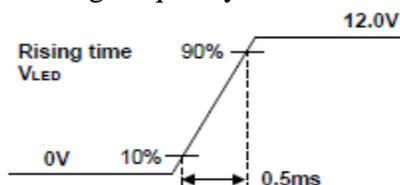
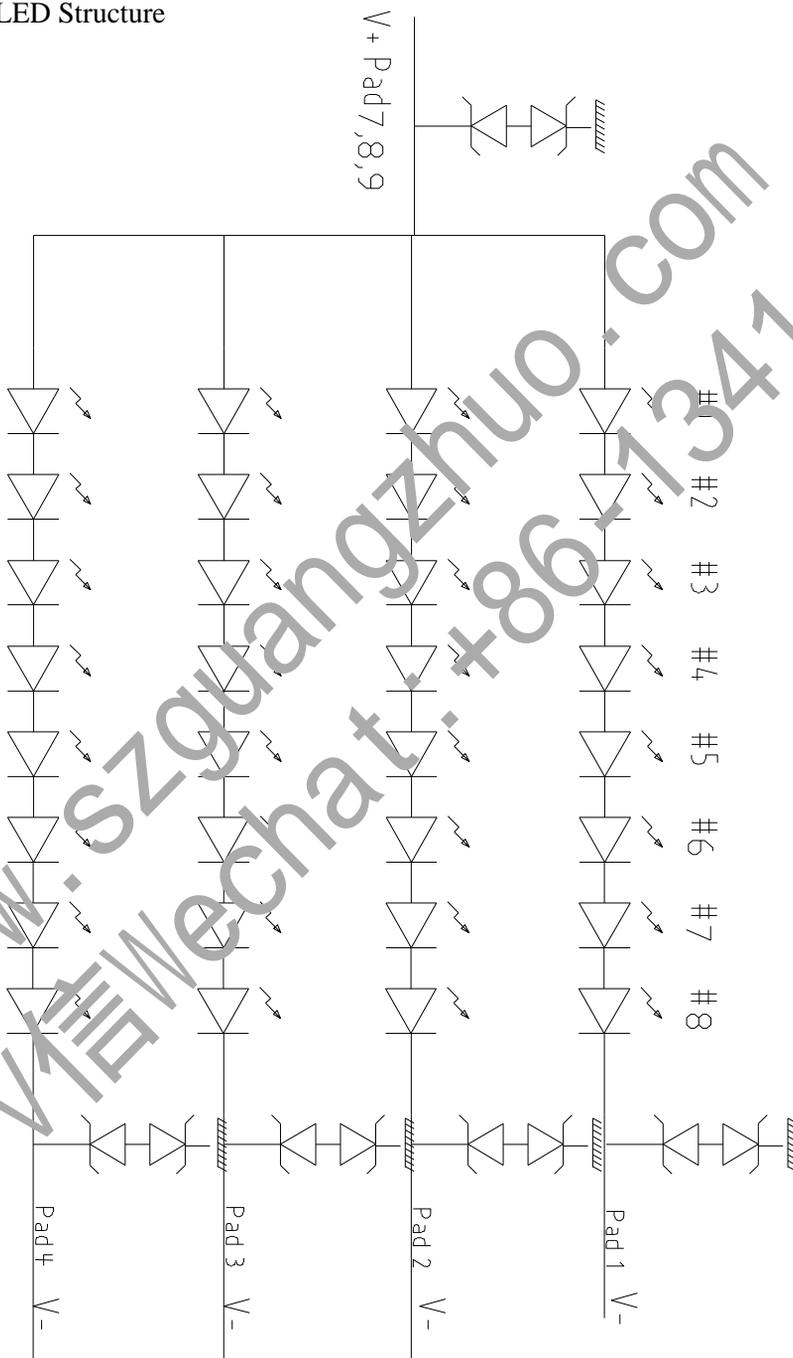


Figure 5. Inrush Measure Condition

3.3 LED Structure

Figure 6. LED Structure



4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25 \pm 2^\circ\text{C}$) with the equipment of luminance meter system (PR730&PR810) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and ϕ equal to 0° . We refer to $\theta=0$ ($=\theta_3$) as the 3 o'clock direction (the "right"), $\theta=90$ ($=\theta_{12}$) as the 12 o'clock direction ("upward"), $\theta=180$ ($=\theta_9$) as the 9 o'clock direction ("left") and $\theta=270$ ($=\theta_6$) as the 6 o'clock direction ("bottom"). While scanning θ and/or ϕ , the center of the measuring spot on the display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement. VDD shall be $3.3 \pm 0.3\text{V}$ at 25°C . Optimum viewing angle direction is 6 o'clock.

4.2 Optical Specifications

<Table 5. Optical Specifications>

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle Range	Horizontal	θ_3	$\theta = 0^\circ$ $\phi = 10^\circ$	-	45	-	Deg.	Note 1
		θ_9		-	45	-	Deg.	
	Vertical	θ_{12}		-	20	-	Deg.	
		θ_6		-	40	-	Deg.	
Luminance Contrast Ratio		CP	$\theta = 0^\circ$	-	400	-		Note 2
Luminance of White	5 Points	Y_w	$\theta = 0^\circ$ $I_{LED} = 21\text{mA}$	-	220	-	cd/m ²	Note 3
White Luminance Uniformity	5 Point	ΔY_5		80%	-	-		Note 4
	13 Points	ΔY_{13}		65%	-	-		
White Chromaticity		W_x	$\theta = 0^\circ$	0.283	0.313	0.343		Note 5
		W_y		0.299	0.329	0.359		
Reproduction of Color	Red	R_x	$\theta = 0^\circ$	-0.03	0.589	+0.03		
		R_y			0.343			
	Green	G_x			0.330			
		G_y			0.570			
	Blue	B_x			0.157			
		B_y			0.137			
Color Gamut				-	45	-	%	
Response Time (Rising + Falling)		T_{RT}	$T_a = 25^\circ\text{C}$ $\theta = 0^\circ$	-	25	-	ms	Note 6
Cross Talk		CT	$\theta = 0^\circ$	-	-	2.0	%	Note 7

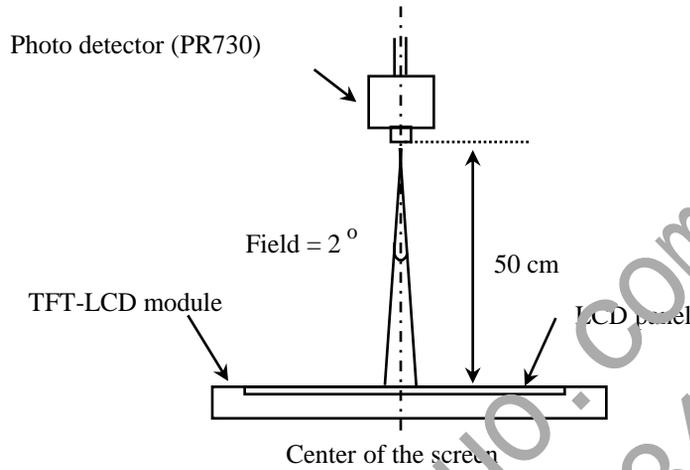
Notes :

- Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see Figure 7).
- Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (see Figure 7) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

- Center Luminance of white is defined as luminance values of 5 point average across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in Figure 8 for a total of the measurements per display.
- The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = \text{Minimum Luminance of 5(or 13) points} / \text{Maximum Luminance of 5(or 13) points}$. (see Figure 8 and Figure 9).
- The color chromaticity coordinates specified in Table 5 shall be calculated from the spectral data measured with all pixels, first in red, green, blue and white. Measurements shall be made at the center of the panel.
- The electro-optical response time measurements shall be made as Figure 10 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_f .
- Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark. (See Figure 11).

4.3 Optical Measurements



Optical characteristics measurement setup

Figure 7. Measurement Set Up

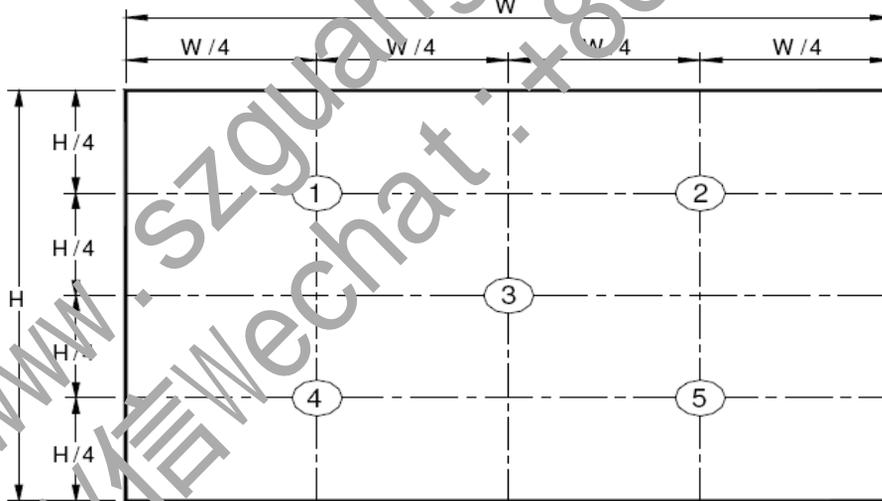


Figure 8. White Luminance and Uniformity Measurement Locations (5 points)

Center Luminance of white is defined as luminance values of center 5 points across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in Figure 7 for a total of the measurements per display.

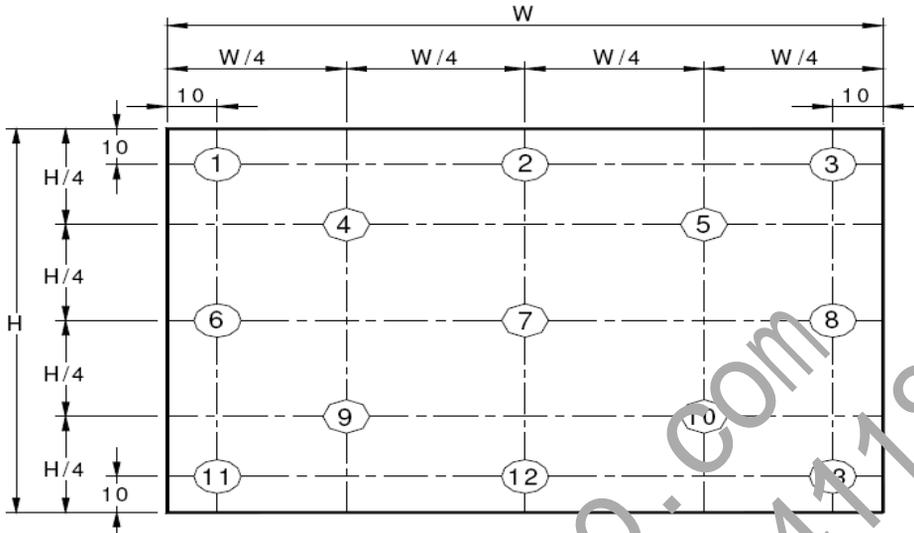


Figure 9. Uniformity Measurement Locations (13 points)

The White luminance uniformity on LCD surface is then expressed as : $\Delta Y5$ = Minimum Luminance of five points / Maximum Luminance of five points (see Figure 8), $\Delta Y13$ = Minimum Luminance of 13 points / Maximum Luminance of 13 points (see Figure 9).

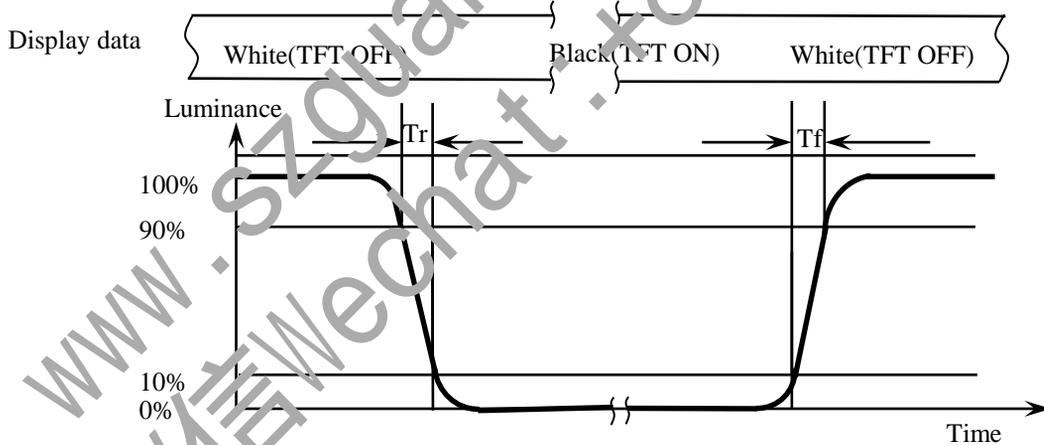
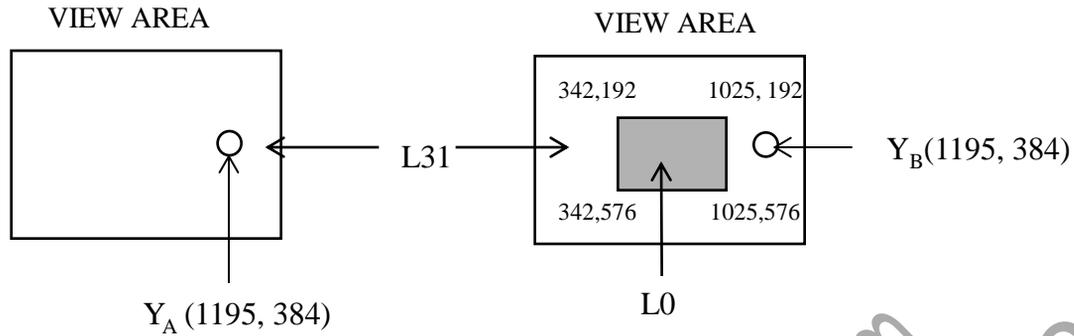


Figure 10. Response Time Testing

The electro-optical response time measurements shall be made as shown in Figure 10 by switching the “data” input signal ON and OFF. Tr: The luminance to change from 90% to 10% ,Tf: The luminance to change from 10% to 90% .



$$\text{Cross Talk (\%)} = \left| \frac{Y_B - Y_A}{Y_A} \right| \times 100$$

Figure 11. Cross Talk Modulation Test Description

Where:

Y_A = Initial luminance of measured area (cd/m^2)

Y_B = Subsequent luminance of measured area (cd/m^2)

The location 1/2/3/4 measured will be exactly the same in both patterns. The test background gray is from L64 to L192. Take the largest data as the result.

Cross Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark. (Refer to Figure 11)

The test system: PR730

5.0 INTERFACE CONNECTION

5.1 Electrical Interface Connection

The electronics interface connector is STM MSAK24025P30.

The connector interface pin assignments are listed in Table 6.

<Table 6. Pin Assignments for the Interface Connector>

Terminal	Symbol	Functions
Pin No.	Symbol	Description
1	CABC_ENABLE	NC
2	H_GND	Ground
3	LANE1_N	NC
4	LANE1_P	NC
5	H_GND	Ground
6	LANE0_N	eDP RX channel 0 negative
7	LANE0_P	eDP RX channel 0 positive
8	H_GND	Ground
9	AUX_CH_P	eDP AUX CH positive
10	AUX_CH_N	eDP AUX CH negative
11	H_GND	Ground
12	LCD_VCC	Power Supply, 3.3V (typ.)
13	LCD_VCC	Power Supply, 3.3V (typ.)
14	BISTC	Panel self test enable
15	H_GND	Ground
16	H_GND	Ground
17	rPD	Hot plug detect output
18	BL_GND	LED Ground
19	BL_GND	LED Ground
20	BL_GND	LED Ground
21	BL_GND	LED Ground
22	BL_ENABLE	LED enable pin(+3.3V Input)
23	BL_PWM	System PWM Signal Input
24	Hsnyc	NC
25	COLOR_ENABLE	NC
26	BL_POWER	LED Power Supply 6V-21V
27	BL_POWER	LED Power Supply 6V-21V
28	BL_POWER	LED Power Supply 6V-21V
29	BL_POWER	LED Power Supply 6V-21V
30	NC	NC

5.2 eDP Interface

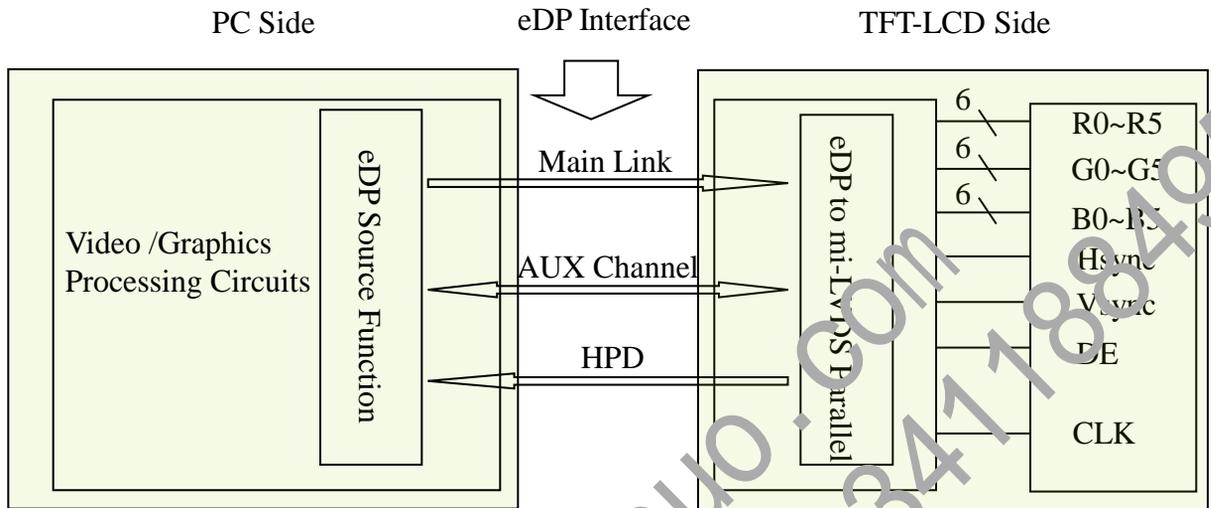


Figure 12. eDP Interface Architecture

Note:

Transmitter : Parade DP501 or equivalent.

Transmitter is not contained in module.

5.3 Data Input Format

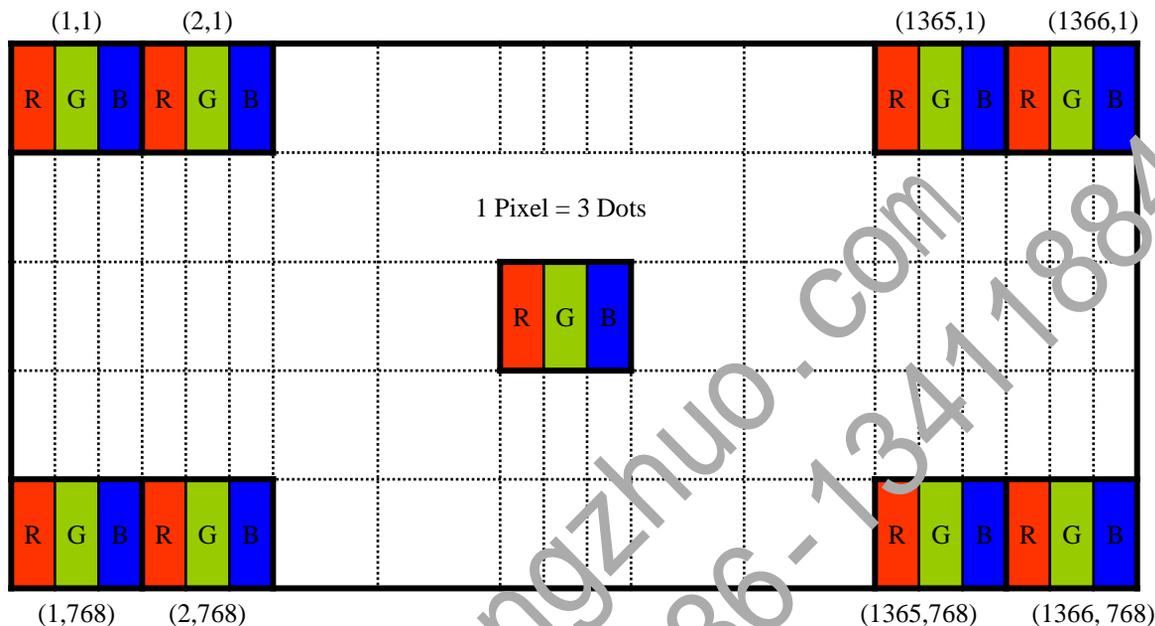


Figure 15. Display Position of Input Data (V-H)

5.4 Back-light & LCM Interface Connection

BLU Interface Connector: STM MSK24022P10.

<Table 7. Pin Assignments for the BLU Connector>

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	LED	LED cathode connection	6	NC	No Connection
2	LED	LED cathode connection	7	NC	No Connection
3	LED	LED cathode connection	8	Vout	LED anode connection
4	LED	LED cathode connection	9	Vout	LED anode connection
5	NC	No Connection	10	Vout	LED anode connection

6.0 SIGNAL TIMING SPECIFICATION

6.1 The NT140WHM-N34 Is Operated By The DE Only

< Table 8. Signal Timing Specification >

Item		Symbols	Min	Typ	Max	Unit
Clock	Frequency	1/Tc	69.3	72.4	75	MHz
Frame Period		Tv	780	790	810	lines
			60	60	60	Hz
			-	16.67	-	ms
Vertical Display Period		Tvd	-	768	-	lines
One line Scanning Period		Th	1486	1528	1550	clocks
Horizontal Display Period		Phi	-	1366	-	clocks

Note : The above is as optimized setting.

6.2 eDP Rx Interface Timing Parameter

The specification of the eDP Rx interface timing parameter is shown in Table 9.

<Table 9. eDP Main-Link RX TP4 Package Pin Parameters>

Item	Symbol	Min	Typ	Max	Unit	Remark
Spread spectrum clock (Link clock down-spreading)	ssc	0	-	0.5	%	
Differential peak-to-peak input voltage at package pins	VRX-DIFFp-p	100	-	1320	mV	
Rx input DC common mode voltage	VRX_DC_CM	0	-	2	V	
Differential termination resistance	RRX-DIFF	80	-	120	Ω	
Single-ended termination resistance	RRX-SE	40	-	60	Ω	
Rx short circuit current limit	IRX_SHORT	-	-	50	mA	
Intra-pair skew at Rx package pins (HBR) RX intra-pair skew tolerance at HBR	LRX_SKEW_ INTRA_PAIR	-	-	60	ps	

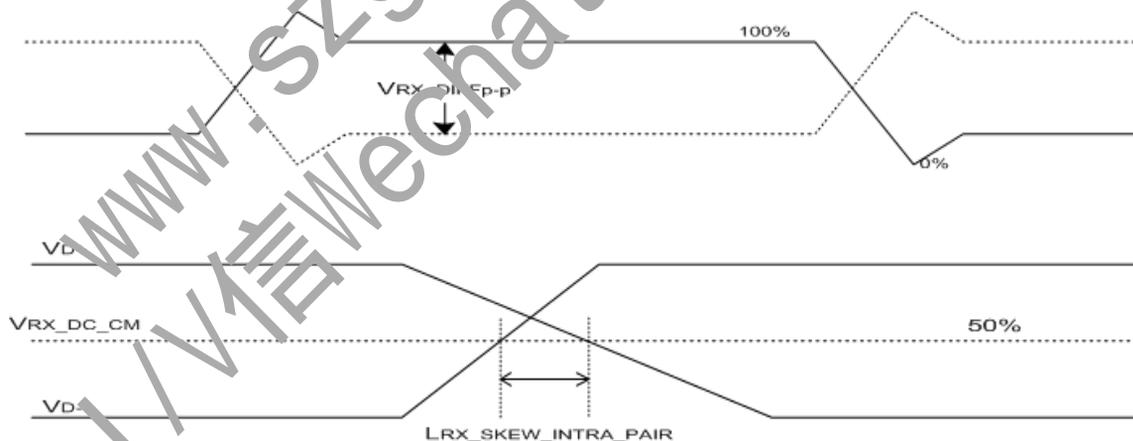


Figure 14. VRX-DIFFp-p & LRX_SKEW_INTRA_PAIR

7.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

<Table 10. Input Signal & Basic Display Colors & Gray Scale of Colors >

	Colors & Gray scale	Data signal																	
		R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	B4	B5
Basic colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Light Blue	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Purple	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray scale of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Darker	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△																		
	▽																		
	Brighter	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	
▽	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0		
Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0		
Gray scale of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	
	Darker	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	
	△																		
	▽																		
	Brighter	0	0	0	0	0	0	1	0	1	1	1	0	0	0	0	0	0	
▽	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0		
Green	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0		
Gray scale of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
	△																		
	▽																		
	Brighter	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	
▽	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1		
Blue	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1		
Gray scale of White & Black	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	1	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	
	Darker	0	1	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	
	△																		
	▽																		
	Brighter	1	0	1	1	1	1	1	0	1	1	1	1	0	1	1	1	1	
▽	0	1	1	1	1	1	0	1	1	1	1	0	1	1	1	1	1		
White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		

8.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below.

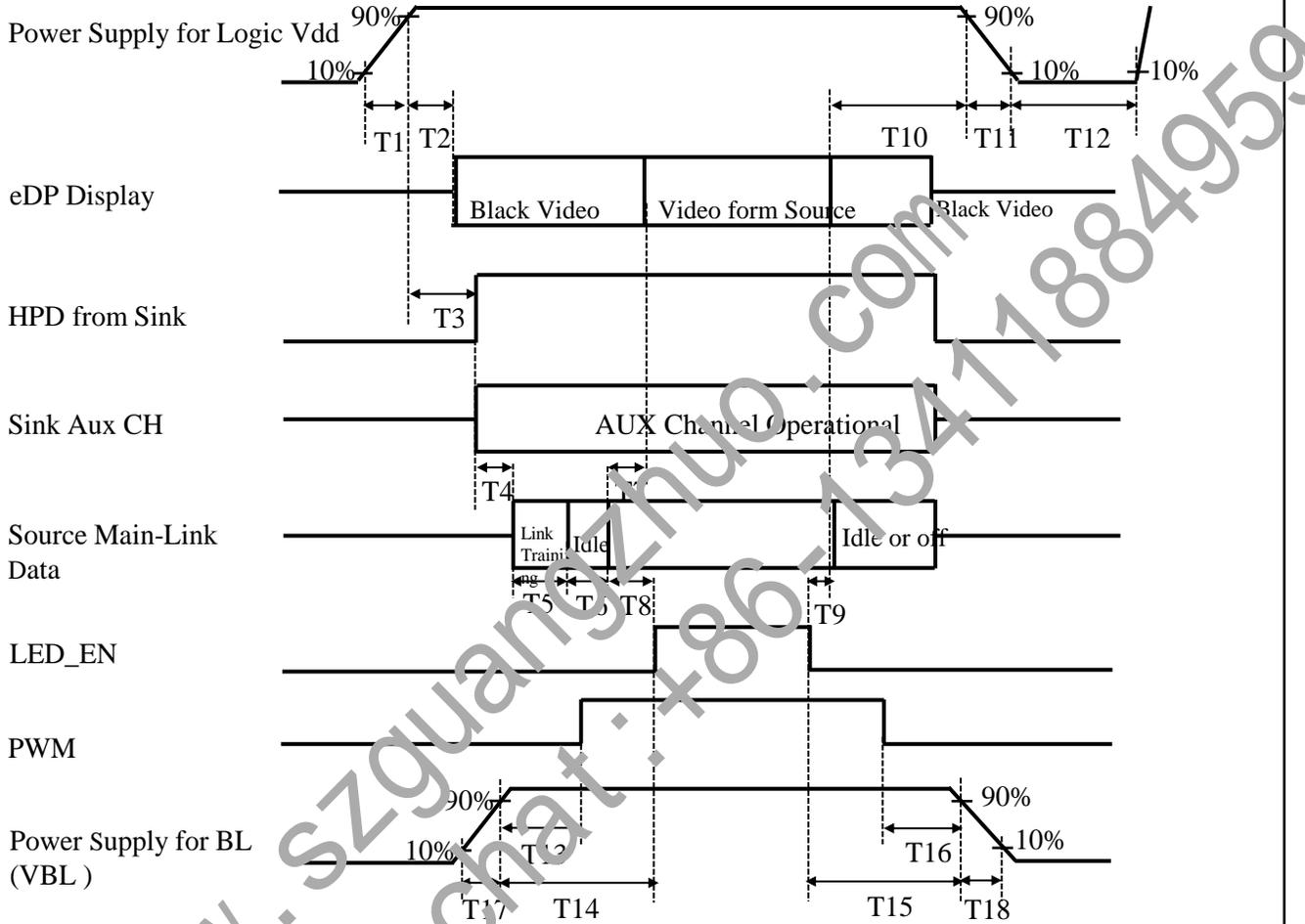


Figure 15. Power Sequence

- $0.5ms \leq T1 \leq 10ms$
- $0ms < T2 \leq 200ms$
- $0ms < T3 \leq 200ms$
- $T3+T4+T5+T6+T8 > 200ms$
- $0ms < T7 \leq 50ms$
- $T7 < T8$
- $0ms < T9$
- $0ms < T10 < 500ms$
- $0.5ms \leq T11 \leq 10ms$
- $500ms \leq T12$
- $0ms < T13$
- $0ms < T14$
- $0ms < T15$
- $0ms < T16$
- $0.5ms \leq T17$
- $0.5ms \leq T18$

Note:

- When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid.

9.0 Connector Description

Physical interface is described as for the connector on LCM.

These connectors are capable of accommodating the following signals and will be following components.

9.1 TFT LCD Module

< Table 11. Signal Connector >

Connector Name /Description	For Signal Connector
Manufacturer	I-PEX
Type/ Part Number	20455-030 E-66
Mating housing/ Part Number	I-PEX 20454-030T

10.0 MECHANICAL CHARACTERISTICS

10.1 Dimensional Requirements

Figure 20 shows mechanical outlines for the model NT140WHM-N34.
Other parameters are shown in Table 12.

<Table 12. Dimensional Parameters>

Parameter	Specification	Unit
Active Area	309.4 (H) × 173.95 (V)	mm
Number of pixels	1366 (H) X 768 (V) (1 pixel = R + G + B dot)	pixels
Pixel pitch	0.07546(H) * 0.072629(V)	mm
Pixel arrangement	RGB Vertical stripe	
Display colors	262K(6bit)	
Display mode	Normally white	
Dimensional outline	316.2x193.05 (With PCB, Typ)x.3.0(Max)	mm
Weight	265 (max)	g

10.2 Mounting

See Figure 20.

10.3 Glare and Polarizer Hardness

The surface of the LCD has a Glare coating to maximize readability and hard coating to reduce scratching.

10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

11.0 RELIABILITY TEST

The reliability test items and its conditions are shown in below.

<Table 13. Reliability Test>

No	Test Items	Conditions
1	High temperature storage test	Ta = 60°C , 60%RH, 240 hrs
2	Low temperature storage test	Ta = -20°C , 240 hrs
3	High temperature & high humidity operation test	Ta = 50°C , 80%RH, 240 hrs
4	High temperature operation test	Ta = 50°C , 60%RH, 240 hrs
5	Low temperature operation test	Ta = 0°C , 240 hrs
6	Thermal shock	Ta = -20 °C ↔ 60 °C (0.5 h), 60% ± 3%RH, 100 cycle
7	Vibration test (non-operating)	Ta = 25 °C , 60%RH ± 5% 10~500Hz, Half Sine X,Y,Z / Sweep rate : 1 hour
8	Shock test (non-operating)	Ta = 25 °C , 60%RH, 220G, Half Sine Wave 2 msec ± X, ± Y, ± Z Once for each direction
9	Electro-static discharge test (non-operating)	Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV Ta = 25 °C , 60%RH,

12.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protector sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

BOE	PRODUCT GROUP	REV	ISSUE DATE
	Customer Spec	P0	2018.01.31

(4) Cautions for the atmosphere

- Dew drop atmosphere should be avoided.
- Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.

(5) Cautions for the module characteristics

- Do not apply fixed pattern data signal to the LCD module at product aging.
- Applying fixed pattern for a long time may cause image sticking.

(6) Other cautions

- Do not disassemble and/or re-assemble LCD module.
- Do not re-adjust variable resistor or switch etc.
- When returning the module for repair or etc. Please pack the module not to be broken. We recommend to use the original shipping packages.

13.0 LABEL

(1) Product Label



Figure 16. Product Label

SPEC. NUMBER	SPEC. TITLE	PAGE
www.szguangzhuo.com 广卓液晶屏 echodeng61@hotmail.com jimei@jimeihk.com +86-13411884959	NT140WHM-N34 Product Specification Rev. P0	26 OF 33

(2) High voltage caution label

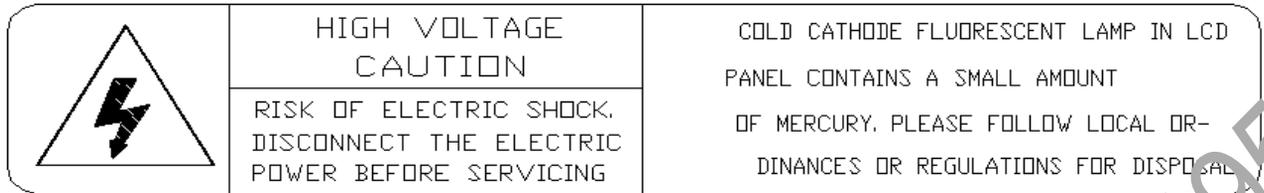


Figure 17. High Voltage Caution Label

(3) Box Label

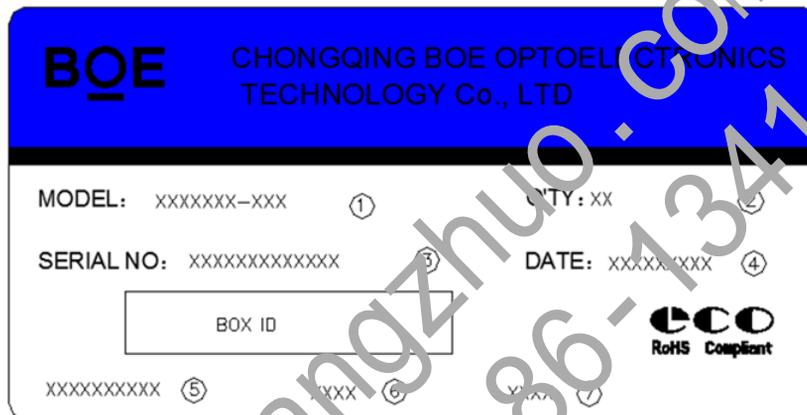


Figure 18. Box Label

Serial number marked part needs to print, show as follows:

1. FG-CODE(Before 12 bit)
2. Product quantity
3. Box ID
4. Date
5. The client section material number(The client)
6. FG-Code: After four -
7. The supplier code

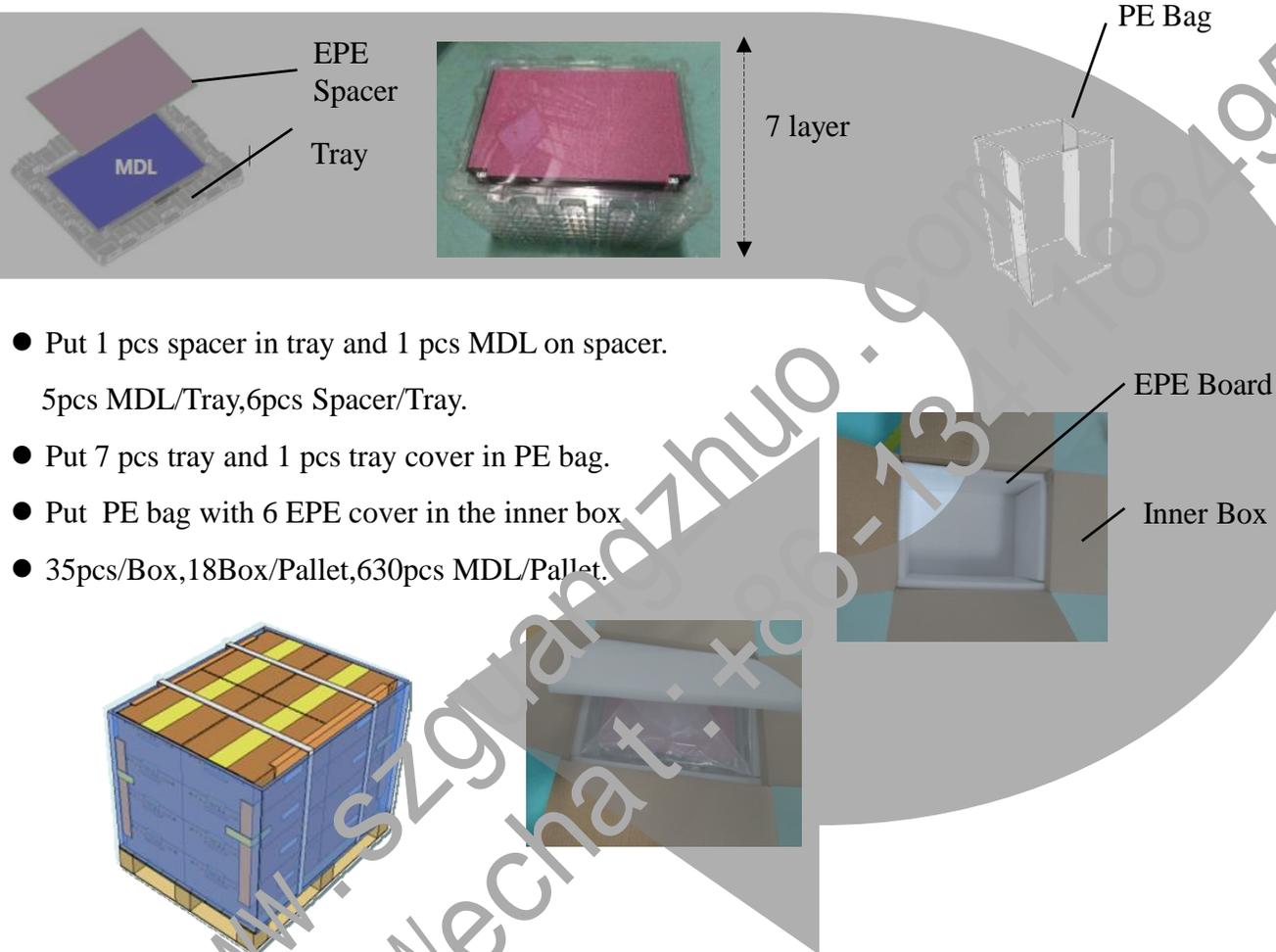
Total size:100×50mm

<Table 14. Box Label Naming Rule >

Digit Code	1	2	3	4	5	6	7	8	9	10	11	12	13
Code	B	9	A	F	1	7	8	N	0	0	3	2	7
Description	Product Name		Product Grade	B8	Year		Month	Revision	BOX Serial Number				

14.0 PACKING INFORMATION

14.1 Packing Order



- Put 1 pcs spacer in tray and 1 pcs MDL on spacer.
5pcs MDL/Tray, 6pcs Spacer/Tray.
- Put 7 pcs tray and 1 pcs tray cover in PE bag.
- Put PE bag with 6 EPE cover in the inner box.
- 35pcs/Box, 18Box/Pallet, 630pcs MDL/Pallet.

Figure 19. Packing Order

14.2 Note

- Box dimension: 480mm*350mm*285mm
- Package quantity in one box: 35pcs
- Total weight: 15.7kg/Box

15.0 MECHANICAL OUTLINE DIMENSION

Figure 20. Outline Dimensions (Front view)

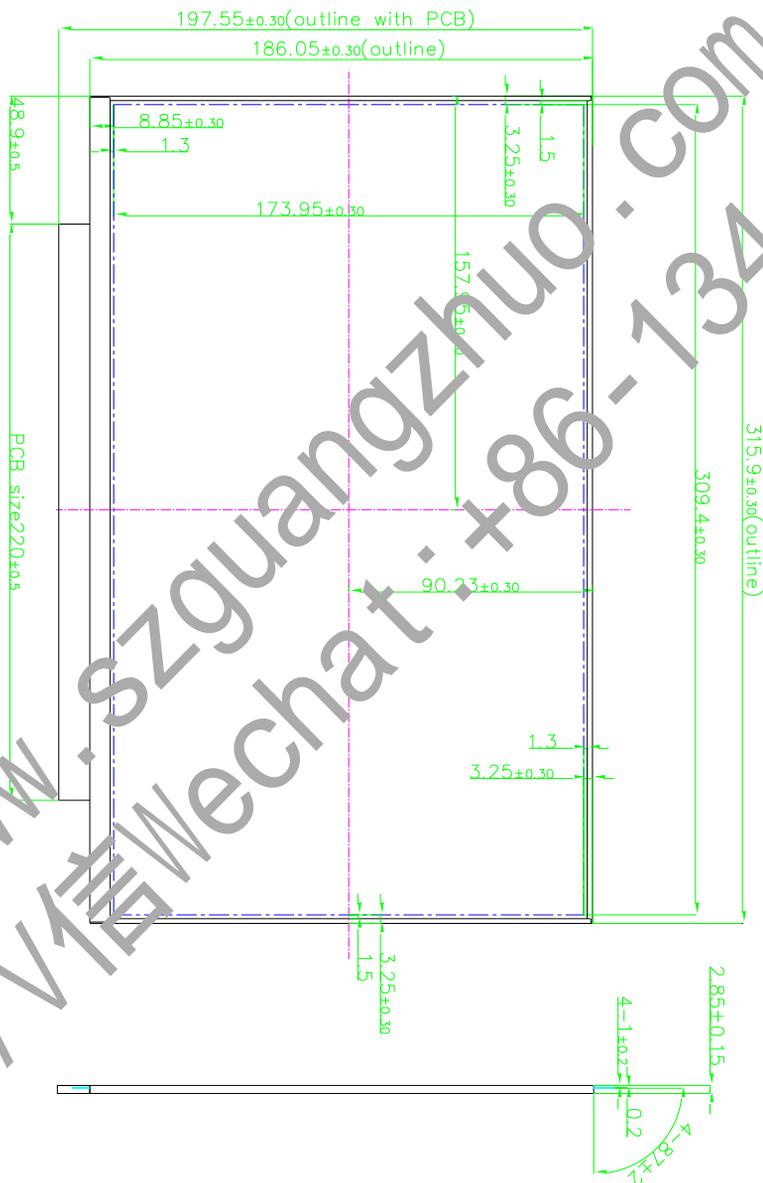
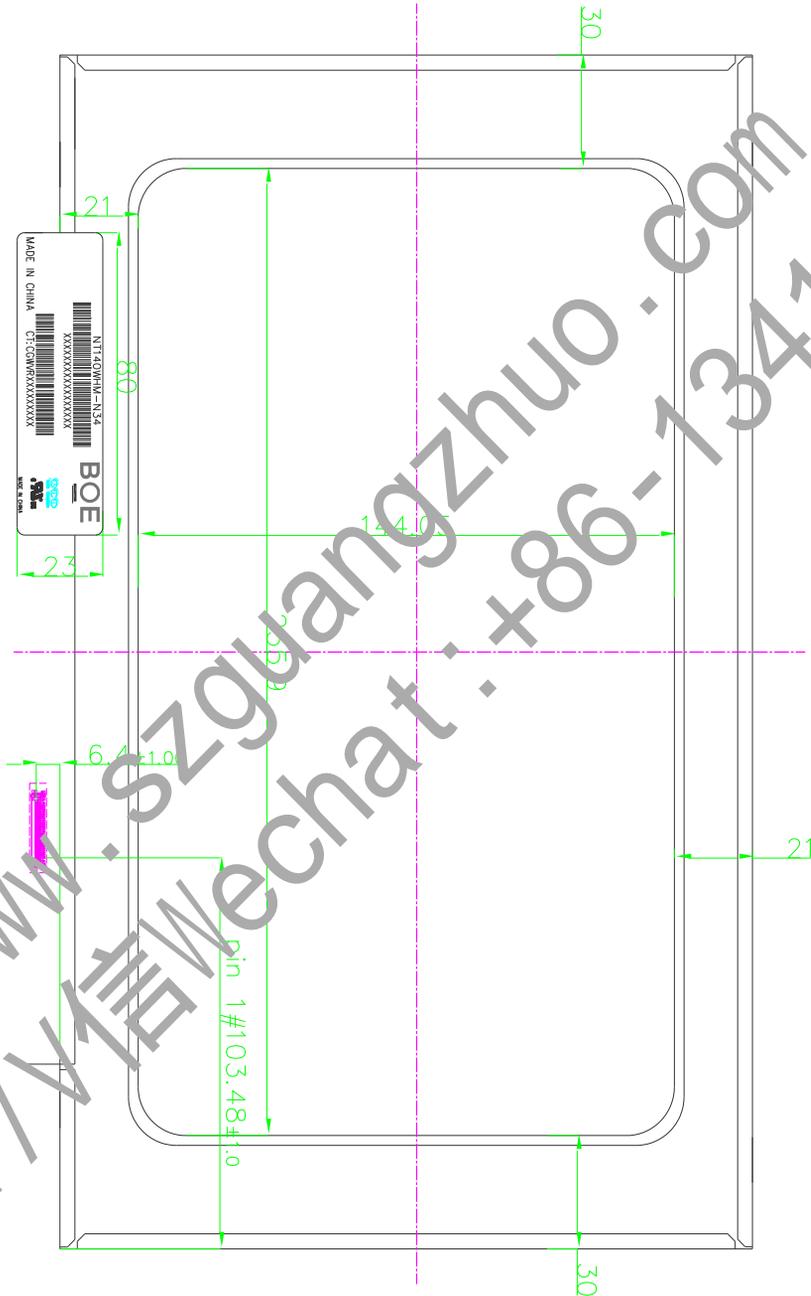


Figure 21. Outline Dimensions (Rear view)



16.0 EDID Table

Address (HEX)	Function	Hex	Dec	crc	Input values.	Notes
00	Header	00	0		0	EDID Header
01		FF	255		255	
02		FF	255		255	
03		FF	255		255	
04		FF	255		255	
05		FF	255		255	
06		FF	255		255	
07		00	0		0	
08	ID Manufacturer Name	09	9		BOE	ID = BOE
09		E5	229			
0A	ID Product Code	B5	181		1973	ID = 1973
0B		07	7			
0C	32-bit serial No.	00	0		0	
0D		00	0		0	
0E		00	0		0	
0F		00	0		0	
10	Week of manufacture	05	5		5	
11	Year of Manufacture	1C	28		2018	Manufactured in 2018
12	EDID Structure Ver.	01	1		1	EDID Ver 1.0
13	EDID revision #	04	4		4	EDID Rev. 0.4
14	Video input definition	95	149		-	Refer to right table
15	Max H image size	1E	30		-	30.9 cm (Approx)
16	Max V image size	11	17		17	17.3 cm (Approx)
17	Display Gamma	78	120		2.2	Gamma curve = 2.2
18	Feature support	02	2		-	Refer to right table
19	Red/Green low bits	F7	247		-	Red / Green Low Bits
1A	Blue/White low bits	00	0		-	Blue / White Low Bits
1B	Red x high bits	91	150	603	0.589	Red (x) = 10010110 (0.589)
1C	Red y high bits	57	87	351	0.343	Red (y) = 01010111 (0.343)
1D	Green x high bits	54	84	337	0.330	Green (x) = 01010100 (0.33)
1E	Green y high bits	92	144	583	0.570	Green (y) = 10010010 (0.57)
1F	Blue x high bits	28	40	160	0.157	Blue (x) = 00101000 (0.157)
20	Blue y high bits	23	35	140	0.137	Blue (y) = 00100011 (0.137)
21	White x high bits	50	80	320	0.313	White (x) = 01010000 (0.313)
22	White y high bits	54	84	336	0.329	White (y) = 01010100 (0.329)
23	Established timing #1	00	0		-	Refer to right table
24	Established timing #2	00	0		-	
25	Established timing #3	00	0		-	
26	Standard timing #1	01	1			Not Used
27		01	1			
28	Standard timing #2	01	1			Not Used
29		01	1			
2A	Standard timing #3	01	1			Not Used
2B		01	1			

2C	Standard timing #4	01	1			Not Used
2D		01	1			
2E	Standard timing #5	01	1			Not Used
2F		01	1			
30	Standard timing #6	01	1			Not Used
31		01	1			
32	Standard timing #7	01	1			Not Used
33		01	1			
34	Standard timing #8	01	1			Not Used
35		01	1			
36	Detailed timing/monitor descriptor #1	4A	74		72.4	72.4272MHz Main clock
37		1C	28			
38		56	86		1366	Hor Active = 1366
39		A2	162		162	Hor Blanking = 162
3A		50	80		-	4 bits of Hor. Active + 4 bits of Hor. Blanking
3B		00	0		768	Ver Active = 768
3C		16	22		22	Ver Blanking = 22
3D		30	48		-	4 bits of Ver. Active + 4 bits of Ver. Blanking
3E		30	48		48	Hor Sync Offset = 48
3F		20	32		32	H Sync Pulse Width = 32
40		36	54		3	V sync Offset = 3 line
41		00	0		6	V Sync Pulse width : 6 line
42		35	53		309	Horizontal Image Size = 309 mm (Low 8 bits)
43		AD	173		173	Vertical Image Size = 173 mm (Low 8 bits)
44		10	16		-	4 bits of Hor Image Size + 4 bits of Ver Image Size
45		00	0		0	Hor Border (pixels)
46		00	0		0	Vertical Border (Lines)
47		1A	26		-	Refer to right table
48		DC	220		48.3	48.2848MHz Main clock
49		12	18			
4A	56	86		1366	Hor Active = 1366	
4B	A2	162		162	Hor Blanking = 162	
4C	50	80		-	4 bits of Hor. Active + 4 bits of Hor. Blanking	
4D	00	0		768	Ver Active = 768	
4E	16	22		22	Ver Blanking = 22	
4F	30	48		-	4 bits of Ver. Active + 4 bits of Ver. Blanking	
50	30	48		48	Hor Sync Offset = 48	
51	20	32		32	H Sync Pulse Width = 32	
52	36	54		3	V sync Offset = 3 line	
53	00	0		6	V Sync Pulse width : 6 line	
54	35	53		309	Horizontal Image Size = 309 mm (Low 8 bits)	
55	AD	173		173	Vertical Image Size = 173 mm (Low 8 bits)	
56	10	16		-	4 bits of Hor Image Size + 4 bits of Ver Image Size	
57	00	0		0	Hor Border (pixels)	
58	00	0		0	Vertical Border (Lines)	
59	1A	26		-	Refer to right above table	

5A	Detailed timing/monitor descriptor #3	00	0			<p>Nvidia nvDPS (Refer the tab of nvDPS)</p> <p>Lowest refresh rate that does not cause any visual/optical side effect</p>
5B		00	0			
5C		00	0			
5D		00	0			
5E		00	0			
5F		00	0			
60		00	0			
61		00	0			
62		00	0			
63		00	0			
64		00	0			
65		00	0			
66		00	0			
67		00	0			
68	00	0				
69	00	0				
6A	00	0				
6B	00	0				
6C	Detailed timing/monitor descriptor #4	00	0			Detailed Timing Description #4
6D		00	0			Flag
6E		00	0			Reserved
6F		02	2			For Brightness Table and Power consumption
70		00	0			Flag
71		0C	12		-	PWM % [7:0] @ Step 0
72		49	73		-	PWM % [7:0] @ Step 5
73		FF	255		-	PWM % [7:0] @ step 10
74		0A	10		-	Nits [7:0] @ Step 0
75		3C	60		-	Nits [7:0] @ Step 5
76		6E	110		-	Nits [7:0] @ Step 10
77		11	17		-	Panel Electronics Power @32x32 Chess Pattern = 700mW
78		0F	15		-	Backlight Power @60 nits = 635.294117647059mW
79		1B	27		-	Backlight Power @Step 10 = 2200mW
7A	6E	110		-	Nits @ 100% PWM Duty = 220nit	
7B	00	0			Format :	
7C	00	0			terminate with ASCII code 0Ah	
7D	00	0			and pad field with ASCII code 20h	
7E	Extension flag	00	0		1	0 : 1个EDID ; N-1 : N个EDID
7F	Checksum	1B	232	232	-	